



PCIe Protocol Suite™ 8.10 Release Notes

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1. Overview

This Read Me file contains last-minute product information for the PCIe Protocol Suite™ software for Microsoft Windows. For full instructions on using the software and the hardware, please see the User's Manual and other documents provided with this product.

2. System Requirements

The following is a list of recommendations for the host machine that runs the PCIe Protocol Suite application and that is connected to the Summit systems.

For optimal performance, it is recommended that the software is installed and operated on a host machine that is of a recent generation of personal computers architecture. However, the software should also operate on machines that are below the minimum requirements specified here, at the cost of slower performance, provided that memory, storage and display requirement are kept.

2.1. Software

Operating System:

- Windows 10, Windows 8 (x86 and x64), Windows Server 2012 (x64), Windows 7 (x86 and x64), Windows Server 2008R2 (x64).
- The latest Service Pack available for the Windows OS in use is required.
- It is recommended that you use one of the supported 64-bit Windows versions listed above as they allow using more RAM than the 32-bit ones.

Required applications:

Microsoft Internet Explorer, version 6 or newer. To view the manuals, datasheets and other documents, you would need to install 'Adobe Acrobat Reader' (<http://get.adobe.com/reader>).

2.2. Hardware

Memory (RAM):

This software application may use up to 4GB of the RAM in the host machine. For improved performance of the software, it is recommended that 16GB of RAM is installed on the host machine. Memory as little as 2GB would still allow the software to function, but would limit its performance and user experience.

Non-volatile Storage (SDD or Hard Disk):

- Storage space of 1GB is required for installing the PCIe Protocol Suite software on the host machine.
- Additional storage space is needed for the operation of the software application and for storing recorded data in files.
- Please remember that storing large captured traces can result in multiple gigabytes of file sizes and can quickly fill your available storage space.

Display:

- To take full advantage of the rich visualization and analysis of Teledyne LeCroy software it is recommended that the display is set to at least 1050 lines of vertical resolution with at least 24-bit color depth.
- The minimum requirement for the display is a resolution of 1024x768 with at least 16-bit color depth.

Connectivity:

- It is recommended that a Gigabit (1000Mbps) Ethernet or a USB3.0 link is used for the connection with the Summit analyzers.
- At minimum the host machine should have either a 100/1000Mbps Ethernet connection to the network or a USB2.0 port.
- If multiple analyzers are daisy chained and connected to the same host machine, one Ethernet connection or one USB port is required for each analyzer.
- Please note that there is no connectivity requirement if the analysis application is used to only view pre-recorded traces.
- For tips as for how to improve on the performance of the Teledyne LeCroy analysis system and more specifically on the performance of the software, please refer to the User Manual.

3. Release Notes

3.1. What's New

- Support for All Analyzers/Exercisers and Z416.
- Support for SMBus/MCTP/NVMe-MI recording in Summit T34.
- Support for SMBus/MCTP/NVMe-MI command generation in Summit Z3-16 with SMBus Support.
- Added NVMe-MI Compliance Test Suite in Script Automation Test Tool.
- Adding new features for Z416 : More trigger options added, LTSSM Control GUI (LTSSM Log, Test Arcs, speed change, LTSSM Control options) and Error Injection script.
- New PLDM and NC-SI decoding in CATC Trace for MCTP based on specifications:
 - a. DSP0236 Base & Control None : ver 1.2.1, December 3, 2014
 - b. DSP0239 Ids and Codes : ver 1.3.0, March 6, 2015
 - c. DSP0238 PCIe VDM binding ver 1.0.2, December 7, 2014
 - d. DSP0240 PLDM Base Specification: ver 1.0.0, April 23, 2009 excluding PLDMVersionData
 - e. DSP0245 PLDM IDs and Codes Specification : ver 1.1.0, January 26, 2011
 - f. DSP0246 PLDM for SMBIOS Data Transfer : ver 1.0.1, December 11, 2009 excluding SMBIOSStructureData
 - g. DSP0248 PLDM for Platform Monitoring and Control : ver 1.1.0, November 8, 2011 excluding PLDMEventLogData and PDR data

- h. DSP0247 PLDM for BIOS Control and Configuration : ver 1.0.0, April 23, 2009 excluding TableData, BIOSAttributesHandles, AttributeData
 - i. DSP0257 PLDM for FRU Data : ver 1.0.0, 26 October, 2011 excluding FRU Record Table
 - j. DSP0222 NC-SI (Network Controller Sideband Interface) : ver 1.1.0, September 23, 2015
 - k. DSP0261 NC-SI over MCTP Binding : ver 1.1.0, March 21, 2015 only NC-Si over MCTP, no Ethernet over MCTP
- Enhancements to Gen3 calibration.
 - Enhancements to VSE: New math operators.
 - Enhancements to Traces Views.
 - Enhancements to NVMe decoding and utilities.
 - Miscellaneous Enhancements to GUI.
 - Bug Fixes.

3.2. Notes

- Notes for PCIe Protocol Suite Application
 - **Supported Products** – PCIe Protocol Suite version 8.05 and higher supports the following products: Summit Z4-16, Summit T34, Summit T3-16, Summit T3-8, Summit T28, Summit T24, Summit Z3-16, Summit T2-16, Summit Z2-16, Summit, and Eclipse X34,
 - **Local Timers** – Timers defined in the sequencer states can only be controlled by events from either downstream or upstream directions but not both at the same time.
 - **Storage Command Emulation** – Refer to \Public\Documents\LeCroy\PCIe Protocol Suite\Sample Files\Z3-16TrainerScripts for examples on how to create PQI/SOP commands. Use this as guide to create other commands not currently supported by the trainer.
 - **NVMe Drive Emulation** – Drivers and instructions for different OS versions can be found at Users\Public\Documents\LeCroy\PCIe Protocol Suite\Sample Files\Z3-16TrainerScripts\NVMe_DriveEmulation\Drivers. You can find more information about drive emulation in manual PETrainerScriptManual.pdf section 6.10 Config = NVMe. Specification level currently supported for NVMe drive emulation is 1.1a. Linux test and validation was performed only on Ubuntu 14.10.
 - **Storage Decodes Supported** – PCIe Protocol Suite currently supports decoding for NVMe 1.2 (Except Controller Memory Buffer); AHCI 1.3.1; ATA 2.6, PQI R7; SOP Level 5 and SCSI T10 (SPC-4 Rev. 14, SBC-3 Rev. 13, SMC-3 Rev. 11, MMC-6 Rev. 01, SSC-2 Rev. 04, SES-2 Rev. 19b); TCG Core Architecture V2.01 Rev 1.00 (08/05/2015); TCG Enterprise SSC Version 1.01 Revision 1.00 subset for NVMe; Opal Specification V2.01 Revision 1.00 (08/05/2015).
 - **Export to CSV/Text** – Starting with version 7.15 when exporting a trace to CSV or text formats the PCIe Protocol Suite will include time delta field. Starting from 7.38 Beta, there is a new legacy option for export menu, see manual for details.
 - **Z3 with CLKREQ support** – This trainer is supported starting from PETracer 7.00 and newer application versions.
 - **Real Time Statistics** –In order to run this utility, perform a recording with recording options properly set for your link and then run Real Time Statistics. The RTS feature is not supported by Summit T24.
 - **Long Trace** – For Summit T34 Expansion mode, traces can grow as large as 80GB for bit tracer mode when utilizing entire buffer (32 GB). The minimum hard disk space must be able to store this trace sizes. To improve performance when analyzing large trace files, please use the highest performance system available to you. It is strongly recommended to have the host Machine's Physical System RAM and free disk space be at least three times the size of the recording buffer you select. This will allow the application to perform best. Example : If Max Analyzer Buffer Size is 32GB you will need 96 GB in System RAM
You should have at least as much physical RAM in the host as the size of your trace file plus the size of the folder of metadata for that trace file (Identified by having the same filename). You can find this value by checking the File Properties of the folder in Windows Explorer. If your physical RAM is smaller than this, lots of swapping to disk will occur, decreasing the performance greatly.
 - **Application Name Change** – As of version 7.30, PETracer software is renamed to PCIe Protocol Suite. Directories for sample traces, scripts and drivers will be now be found under a new path containing this new name.
 - **NVMe Enhanced Mode** – Please read analyzer manual for description of this recording mode. We have added a section in the analyzer manual about the recommended settings in NVMe enhanced mode to record the multiple controller resets.

- **Precision Timestamp Measurement** – Current implementation in Summit Z3 is intended for functional verification.
- **Summit Z3 NVMe Host Emulation** – NVMe script structures are updated to NVMe specification 1.2
- **Windows 7 and Windows Server 2008 R2** – These operating systems may report the Teledyne LeCroy USB device drivers as being unsigned or untrusted. In fact, the drivers are signed with a newer, more secure signing mechanism that is not recognized by these operating systems. Refer to the following article for more information: <https://support.microsoft.com/en-us/kb/3025925>
- **Win10 Support** – Teledyne LeCroy Protocol tools require the installation and operation of our software on user's desktops or laptops. As we are committed to always supporting the latest host-machine technologies, we are adding limited support for Windows 10 Technical Preview in our software. After Windows 10 is formally released, the next Teledyne LeCroy Protocol software releases will include full support for Windows 10, and at the same time, we will no longer test on nor develop fixes for Windows XP nor Windows Server 2003.
- **NVMe Decoding on Windows 10** – When running the PCIe Protocol Suite under Windows 10 the NVMe decoding may experience some issues. This will be addressed in coming releases.
- **Opening Traces Through Network** – Opening traces from an unreliable network location is not recommended. PCIe Protocol Suite application requires a good quality high-performance connection to the network location in order to correctly load and decode a trace. Instead, the trace can be copied to a host hard drive and perform decoding from this location.
- **Summit Z416** – Generation options are enabled only for X1 physical layer testing. Only basic scripts are supported such as: "Link=", "Config=General" and "Config=Link". Include and Loop constructs when used for the mentioned scripts.
- **MCTP Specifications supported in CATC Trace**
 1. DSP0236 Base & Control None : ver 1.2.1, December 3, 2014
 2. DSP0239 Ids and Codes : ver 1.3.0, March 6, 2015
 3. DSP0238 PCIe VDM binding ver 1.0.2, December 7, 2014
- **PLDM Specifications supported in CATC Trace**
 1. DSP0240 PLDM Base Specification: ver 1.0.0, April 23, 2009 excluding PLDMVersionData
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 3. DSP0246 PLDM for SMBIOS Data Transfer : ver 1.0.1, December 11, 2009 excluding SMBIOSStructureData
 4. DSP0248 PLDM for Platform Monitoring and Control : ver 1.1.0, November 8, 2011 excluding PLDMEventLogData and PDR data
 5. DSP0247 PLDM for BIOS Control and Configuration : ver 1.0.0, April 23, 2009 excluding TableData, BIOSAttributesHandles, AttributeData
 6. DSP0257 PLDM for FRU Data : ver 1.0.0, 26 October, 2011 excluding FRU Record Table
- **NC-SI Specifications supported in CATC Trace**
 1. DSP0222 NC-SI (Network Controller Sideband Interface) : ver 1.1.0, September 23, 2015
 2. DSP0261 NC-SI over MCTP Binding : ver 1.1.0, March 21, 2015 only NC-Si over MCTP, no Ethernet over MCTP

3.3. Known Issues

- Known Problems and Issues PCIe Protocol Suite Application
 - **CATC Walk Enhanced Markers playback of audio/video files** – On some systems, the PCIe Protocol Suite application may not be able to playback audio/video files embedded in markers even if the appropriate media codecs are installed. The root cause is usually a 3rd-party application that installed a faulty filter that prevents the Microsoft DirectShow subsystem from loading the required audio/video filters. One known 3rd-party application that causes this problem is Corel WinDVD v10. The recommended solution is to uninstall Corel WinDVD v10 if it is not being used. If 3rd-party driver is not uninstalled, the PCIe Protocol Suite application will issue a warning message when opening a trace.
 - **CATC Walk on Windows 10** – When an image is set as a marker, this image can't be previewed when mouse is hovered on packet header or while opening manage playlists from View -> CATC Walk and choosing the target image under Markers.
 - **Transaction Level decode** – When decoding a CATC Trace for Link, Split or any storage commands, if the process is interrupted before the trace has been completely processed, the trace must be closed then re-opened to restart the decoding process. This will be corrected in future releases.
 - **Display Options** – In Level Hiding tab, the TLP Data Pattern type mask/match patterns specified are not currently saved. This will be corrected in coming releases.
 - **PETracer 7.10** – When installing PETracer 7.10 and using the default recording options from older software versions, the application may not work properly, in case this happens erase the default.rec file in

the PETracer installation directory, open recording options in the PETracer GUI, do Restore Factory Settings and Save as Default. This problem has been addressed in 7.20 version.

- **PETracer 7.10 Capturing a Full Buffer Trace** – The PETracer will upload the trace but only few segments may be displayed instead of more segments amounting to the total buffer selected. Do a file "Save as", close the trace and open it again, all the segments will show. This is not a problem for later releases because large buffer support feature added does not need segmentation upload for traces.
- **RRAP Items** – Starting with PCIe Protocol Suite 7.30 RRAP tags will show in some of the GUI views for PCIe Analyzer products. This correspond to M-PCIe ECN updates to the PCI Express Specification but do not apply to PCIe Express protocol. These items will be removed for PCIe Analyzers in the future releases.
- **Trigger on Lane Change** – In certain occasions this trigger on Summit analyzers will misfire during speed switch. This will be corrected in the next release.
- **Filtering SKPs** – When filtering out SKP ordered sets along with TS1 or TS2 ordered sets in the advanced sequencer the SKPs will leak when it comes in between TS sequences and will show up in the trace. This will be corrected in future releases.
- **Traces with many errors** – When the analyzer has not been calibrated or properly configured and lots of errors show in the trace, manipulating such trace may cause the application to fault. This will be corrected in future releases.
- **Higher Level Transaction Decoding** – For some situations when link retrain with electrical idle period happens in the middle of a trace it may cause the split level decoding (or storage decoding) to report incorrect transactions. The work around is to split the trace in two parts containing traffic before Link Down and after Link Up events. This will be corrected in future releases.
- **Internet Explorer 11** – PCIe Protocol Suite requires Microsoft patch 2987107 for Internet Explorer, application faults have been observed without this update. Refer to <http://answers.microsoft.com/thread/de92c06f-8962-46e7-b38e-3d49dc21e6ea>
- **Save Search Options** – In the Search > Find dialog when selecting some events under NVMe Transactions and performing a Save, the selection will not be saved in the search options file. This will be corrected in future releases.
- **Auto LinkWidth in Recording Options** – When used in conjunction with AutoSpeed under L0s traffic, the link width will not be determined correctly. To avoid this issue, use fixed speed or fixed link width option when traffic is L0s. This will be corrected in coming releases.
- **Documentation for NVMe XSD Schema** – The Tech Brief available in \CATC\PETracer\AppNotes\TechBrief_Use_of_NVMe_xml_schema.pdf provides guidelines to use XSD approach for decoding NVMe traces and provides an example. The example is not currently provided in the PCIe Protocol Suite 7.34 installation. It will be included in future releases.
- **Ignore Idle Errors** – When checked this option will filter Gen1 and Gen2 compliance pattern but not Gen2 Modified compliance pattern or Gen3 compliance patterns. For the standard compliance pattern any errors detected or extra delay symbols showing up in Lane0 will leak. This will be addressed in future releases.
- **NVMe Decoding for Older Traces** – As part of our continual improvement plan, we have enhanced the error detection capability and can now detect a broader range of errors. This may result in highlighting some additional errors (particularly in our storage targeted protocols). To ensure that the identified high level errors are truly valid issue, please make sure that the information contained in the PCIe SSD Base Address Mapping (found in the Tools menu) is both complete and correct.
- **Hiding TS Ordered Sets** – If after performing a hide of TS ordered sets the trace still shows TS ordered sets, perform a "Save as" of the trace giving a different name, open such trace and perform the hide command again. This behavior for the hide functionality will be modified in future releases.
- **Gen2 Active Interposer** – Summit T3 and Summit T2 analyzers requires the original "Gen2 Active Interposer" x8 or x16 to be revision G or higher. The analyzers will not have a problem when operating with the new "Gen2 Active Interposer with SRIS".
- **Recording Options for NVMe debug** – When using the Protocol Recording in Recording Mode to debug NVMe, make sure the "Ignore Idle Errors" filter is unchecked.
- **Eclipse X34 with MPCle** – Keep using PCIe Protocol Suite Software version 7.41 to work with the Eclipse X34 System. There is a product detection issue in version 8.10 which does not allow the software to operate the unit, this problem will be resolved in the coming releases.
- **SW Application may fault under the following situations:**
 - Opening a second trace after a first trace is opened while using timing calculator.
 - Saving As for a trace right after uploading from analyzer and while progress bar indicates the navigator still updating.These will be addressed in the next releases.

- Known Problems and Issues Summit Z416
 - **Update Device** – With software 8.06 and previous releases after performing Update Device you will need to power cycle the Z416 unit.
 - **ArcTests** – The following tests are currently operational:
 1. Negotiated Data Rate Failure Detected by DUT
 2. Current Data Rate Failure Detected by DUT
 3. Negotiated Data Rate Failure Detected by Trainer
 4. Current Data Rate Failure Detected by Trainer
 5. Recovery Rcvr Lock to Detect
 6. Recovery Rcvr Cfg to Detect
 7. Recovery Idle to Detect
 8. Test_DUT_Gen3_Equalization_Phase0 to Speed (Summit Z416 is host)
 9. Test_DUT_Gen3_Equalization_Phase1 to Speed (Summit Z416 is host)
 10. Test_DUT_Gen3_Equalization_Phase3 to Speed (Summit Z416 is host)
 11. Test_Trainer_Timeout_Gen3_Equalization_Phase1 to Speed (Summit Z416 is host)
 12. Test_Trainer_Timeout_Gen3_Equalization_Phase2 to Speed (Summit Z416 is host)
 13. Test_Trainer_Timeout_Gen3_Equalization_Phase3 to Speed (Summit Z416 is host)
 14. Test DUT Gen4 Equalization 0 to Speed (Trainer is Host)
 15. Test DUT Gen4 Equalization 1 to Speed (Trainer is Host)
 16. Test DUT Gen4 Equalization 3 to Speed (Trainer is Host)
 17. Test Trainer Timeout Gen4 Equalization Phase1 (Trainer is Host)
 18. Test Trainer Timeout Gen4 Equalization Phase2 (Trainer is Host)
 19. Test Trainer Timeout Gen4 Equalization Phase3 (Trainer is Host)

The following tests are not operational because only x1 is supported at this release:

20. SpeedChangAnyLane (all combinations of speed)
21. RecoveryRcvrLockToConfig

The following tests are operational when run when Gen4 speed is not enabled and run from Z416 as host:

Other tests may be operational but not currently giving proper results. This will be addressed in the coming releases.

- Known Problems and Issues Summit T3-16
 - **Auto-Configure Lane Polarity** – For correct polarity detection Summit recording settings has to be initialized before the monitored link is trained.
If performing 8GT/s calibration the polarity results will be lost. The link has to be retrained at Gen1 or Gen2 for polarity of lanes to be recalculated or simply enter the polarity values manually.
 - **EIOS errors** – Errors found in the EIOS packets as the system goes into low power state can be expected and are to be considered a normal part of operation
 - **Trigger Position** – Trigger positioning can be off by a few (up to 6) packets in either direction due to certain real-time constraints with heavy packet loading.
When setting a trigger such as "Enter Electrical Idle" or "Switch to ..." and the Trigger Position marker is set below 50% in the recording options dialog, the downstream direction may be missing traffic in the CATC Trace when there is a speed switch event from Gen1 to Gen2 or Gen3. This condition will be corrected in next releases.
 - **Ethernet to USB switch** – After Summit T3 has been connected to the Ethernet in the PCIe Protocol Suite application, user should go through the process of disconnecting the unit from the network using the PCIe Protocol Suite menu before connecting it back using USB; otherwise the next recording taken may fault the application. This issue would be addressed in coming releases.
 - **Short Electrical idle periods** may not be detected by the analyzer and as a result Link Up/Link Down blocks may not appear in the trace.
 - **Functionality limitations** – Protocol trace mode no longer keeps 10-bit code symbols in the trace file. Use BitTracer mode to see scrambled/encoded symbol values.
 - **Power Off / On** – Once analyzer is powered off you need to wait at least 10 seconds before you power it back on, otherwise the analyzer may not initialize properly.
 - **Connection through USB HUB** – Summit T3 may require manual restart after BusEngine or Firmware update when it is connected to the host PC via USB Hub. Power the unit down and power it back up if

- software does not see the unit after the update.
- **Gen3 Skips from Retimers** – When the SKP size is different than 130bits the SKP size displayed in the viewers may not be the exact same size as the one present in the bus. Also, the 24 bits following the SKP end symbol may not reflect actual bits in the bus, this will be noticeable in G3 Polling.Compliance and during G3 L0 if previous block is a data block.
- **Overclocking** – Only standard PCIe clocking is supported by the analyzers. No overclocking.
- **Ignore Idle Errors Option** – Currently compliance patterns, shown as PATN packets in the CATC Trace viewer, can only be filtered using the recording option “Ignore Idle Errors”. Using option “Ignore Idle Errors” may remove some training sequences just after link comes out of electrical idle.
- **Auto Options** – Autospeed will not work properly for links already in low power states. Autoswizzle needs to see link training sequences (from configuration or recovery) in order to work. Also use fixed link width if the bus is in L0s state, this will be corrected in next releases.
- **Low power states** – For L0s, the analyzer will filter out about 16 FTS due to lock, deskew and synchronization. If deskew is bypassed extra 3 to 4 FTS can be seen as being recovered only in bit tracer mode. During L0s low power traffic some LinkDwn/LinkUp packets might be missing from the trace when the electrical idle period is under 200ns. During L0s low power traffic some of the FTS packets may show with error marks. The PCIe Protocol Suite tries to show as much data as possible after link comes out of electrical idle, this has the side effect of showing packets with errors until the analyzer has locked properly to the data. For L1, for devices that send less than 9 TS1s after exit of L1 these may not be recorded in the trace and sometimes deskew will be an issue if no SKP is part of the training sequence.
- **Power Save Mode** – USB will not automatically reconnect after host system comes back from power save mode. Perform manual connection for the analyzer or turn off the power savings mode.
- **EIOS** – When entering electrical idle during L0s, L1 or speed change, the Electrical Idle Ordered Set may be missing from the CATC trace in some recordings.
- **Negative Time Stamps** – Precision of packet timestamp is 2ns and accuracy is measured as 3.75ns. Traces may show negative idle time periodically in the trace. This is due to minor periodic adjustments in timestamp to prevent drift.
- **Interrupting USB connection** – While PCIe Protocol Suite is running calibration do not unplug the USB cable as application may fault.
- **Gen2 Active Interposer** – Summit T3 and Summit T2 analyzers requires the original “Gen2 Active Interposer” x8 or x16 to be revision G or higher. The analyzers will not have a problem when operating with the new “Gen2 Active Interposer with SRIS”.
- **Known Problems and Issues Summit T3-8**
 - **Auto-Configure Lane Polarity** – For correct polarity detection Summit recording settings has to be initialized before the monitored link is trained. If performing 8GT/s calibration the polarity results will be lost. The link has to be retrained at Gen1 or Gen2 for polarity of lanes to be recalculated or simply enter the polarity values manually.
 - **EIOS errors** – Errors found in the EIOS packets as the system goes into low power state can be expected and are to be considered a normal part of operation
 - **Trigger Position** – Trigger positioning can be off by a few (up to 6) packets in either direction due to certain real-time constraints with heavy packet loading. When setting a trigger such as "Enter Electrical Idle" or "Switch to ..." and the Trigger Position marker is set below 50% in the recording options dialog, the downstream direction may be missing traffic in the CATC Trace when there is a speed switch event from Gen1 to Gen2 or Gen3. This condition will be corrected in next releases.
 - **Ethernet to USB switch** – After Summit T3 has been connected to the Ethernet in the PCIe Protocol Suite application, user should go through the process of disconnecting the unit from the network using the PCIe Protocol Suite menu before connecting it back using USB; otherwise the next recording taken may fault the application. This issue would be addressed in coming releases.
 - **Short Electrical idle periods** – May not be detected by the analyzer and as a result Link Up/Link Down blocks may not appear in the trace.
 - **Functionality limitations** – Protocol trace mode no longer keeps 10-bit code symbols in the trace file. Use BitTracer mode to see scrambled/encoded symbol values.
 - **Power Off / On** – Once analyzer is powered off you need to wait at least 10 seconds before you power it back on, otherwise the analyzer may not initialize properly.
 - **Connection through USB HUB** – Summit T3 may require manual restart after BusEngine or Firmware update when it is connected to the host PC via USB Hub. Power the unit down and power it back up if software does not see the unit after the update.
 - **Buffer Size** – The buffer capacity while using the Summit T3-8 in Expanded mode with link width option set to fixed X8/X4/X2 or X1 is less than the Summit T3-16 buffer capacity using the same link width option settings.

- **Gen3 Skips from Retimers** – When the SKP size is different than 130bits the SKP size displayed in the viewers may not be the exact same size as the one present in the bus. Also, the 24 bits following the SKP end symbol may not reflect actual bits in the bus, this will be noticeable in G3 Polling.Compliance and during G3 L0 if previous block is a data block.
- **Overclocking** – Only standard PCIe clocking is supported by the analyzers. No overclocking.
- **Ignore Idle Errors Option** – Currently compliance patterns, shown as PATN packets in the CATC Tracer viewer, can only be filtered using the recording option “Ignore Idle Errors”. Using option “Ignore Idle Errors” may remove some training sequences just after link comes out of electrical idle.
- **Auto Options** – Autospeed will not work properly for links already in low power states. Autoswizzle needs to see link training sequences (from configuration or recovery) in order to work. Also use fixed link width if the bus is in L0s state, this will be corrected in next releases.
- **Low power states** – For L0s, the analyzer will filter out about 16 FTS due to lock, deskew and synchronization. If deskew is bypassed extra 3 to 4 FTS can be seen as being recovered only in bit tracer mode. During L0s low power traffic some LinkDwn/LinkUp packets might be missing from the trace when the electrical idle period is under 200ns. During L0s low power traffic some of the FTS packets may show with error marks. The PCIe Protocol Suite tries to show as much data as possible after link comes out of electrical idle, this has the side effect of showing packets with errors until the analyzer has locked properly to the data. For L1, for devices that send less than 9 TS1s after exit of L1 these may not be recorded in the trace and sometimes deskew will be an issue if no SKP is part of the training sequence.
- **Power Save Mode** – USB will not reconnect after automatically when host system comes back from power save mode. Perform manual connection for the analyzer or turn off the power savings mode.
- **Update Bus Engine in Expanded Mode** – While T3-8 unit is reinitialized after it has been updated for bus engine the PCIe Protocol Suite application may fault for certain T3-8 units. When this happens just restart the application, the T3-8 unit was successfully updated. This issue would be addressed in coming releases.
- **EIOS** – When entering electrical idle during L0s, L1 or speed change, the Electrical Idle Ordered Set may be missing from the CATC trace in some recordings.
- **Negative Time Stamps** – Precision of packet timestamp is 2ns and accuracy is measured as 3.75ns. Traces may show negative idle time periodically in the trace. This is due to minor periodic adjustments in timestamp to prevent drift.
- **Expansion Mode Operation** – X16 Expansion Mode Operation is not currently supported in the API or for Automated Test Script application.
- **Interrupting USB connection** – While PCIe Protocol Suite is running calibration do not unplug the USB cable as application may fault.
- **Gen2 Active Interposer** – Summit T3 and Summit T2 analyzers requires the original “Gen2 Active Interposer” x8 or x16 to be revision G or higher. The analyzers will not have a problem when operating with the new “Gen2 Active Interposer with SRIS”.
- **Known Problems and Issues Summit T34**
 - **Auto-Configure Lane Polarity** – For correct polarity detection Summit recording settings has to be initialized before the monitored link is trained.
If performing 8GT/s calibration the polarity results will be lost. The link has to be retrained at Gen1 or Gen2 for polarity of lanes to be recalculated or simply enter the polarity values manually.
 - **EIOS errors** – Errors found in the EIOS packets as the system goes into low power state can be expected and are to be considered a normal part of operation
 - **Trigger Position** – Trigger positioning can be off by a few (up to 6) packets in either direction due to certain real-time constraints with heavy packet loading.
When setting a trigger such as "Enter Electrical Idle" or "Switch to ..." and the Trigger Position marker is set below 50% in the recording options dialog, the downstream direction may be missing traffic in the CATC Trace when there is a speed switch event from Gen1 to Gen2 or Gen3. This condition will be corrected in next releases.
 - **Ethernet to USB switch** – After Summit T34 has been connected to the Ethernet in the PCIe Protocol Suite application, user should go through the process of disconnecting the unit from the network using the PCIe Protocol Suite menu before connecting it back using USB; otherwise the next recording taken may fault the application. This issue would be addressed in coming releases.
 - **Short Electrical idle periods** may not be detected by the analyzer and as a result Link Up/Link Down blocks may not appear in the trace.
 - **Power Off / On** – Once analyzer is powered off you need to wait at least 10 seconds before you power it back on, otherwise the analyzer may not initialize properly.
 - **Connection through USB HUB** – Summit T34 may require manual restart after BusEngine or Firmware update when it is connected to the host PC via USB Hub. Power the unit down and power it back up if software does not see the unit after the update.

- **Gen3 Skips from Retimers** – When the SKP size is different than 130bits the SKP size displayed in the viewers may not be the exact same size as the one present in the bus. Also, the 24 bits following the SKP end symbol may not reflect actual bits in the bus, this will be noticeable in G3 Polling.Compliance and during G3 L0 if previous block is a data block.
- **Overclocking** – Only standard PCIe clocking is supported by the analyzers. No overclocking.
- **Ignore Idle Errors Option** – Currently compliance patterns, shown as PATN packets in the CATC Tracer viewer, can only be filtered using the recording option “Ignore Idle Errors”. Using option “Ignore Idle Errors” may remove some training sequences just after link comes out of electrical idle.
- **Auto Options** – Autospeed will not work properly for links already in low power states. Autoswizzle needs to see link training sequences (from configuration or recovery) in order to work. Also use fixed link width if the bus is in L0s state, this will be corrected in next releases.
- **Low power states** – For L0s, the analyzer will filter out about 16 FTS due to lock, deskew and synchronization. If deskew is bypassed extra 3 to 4 FTS can be seen as being recovered only in bit tracer mode. During L0s low power traffic some LinkDwn/LinkUp packets might be missing from the trace when the electrical idle period is under 200ns. During L0s low power traffic some of the FTS packets may show with error marks. The PCIe Protocol Suite tries to show as much data as possible after link comes out of electrical idle, this has the side effect of showing packets with errors until the analyzer has locked properly to the data. For L1, for devices that send less than 9 TS1s after exit of L1 these may not be recorded in the trace and sometimes deskew will be an issue if no SKP is part of the training sequence.
- **Power Save Mode** – USB will not reconnect after automatically when host system comes back from power save mode. Perform manual connection for the analyzer or turn off the power savings mode.
- **Update Bus Engine in Expanded Mode** – While T34 unit is reinitialized after it has been updated for bus engine the PCIe Protocol Suite application may fault for certain T34 units. When this happens just restart the application, the T34 unit was successfully updated. This issue would be addressed in coming releases.
- **EIOS** – When entering electrical idle during L0s, L1 or speed change, the Electrical Idle Ordered Set may be missing from the CATC trace in some recordings.
- **Negative Time Stamps** – Precision of packet timestamp is 2ns and accuracy is measured as 3.75ns. Traces may show negative idle time periodically in the trace. This is due to minor periodic adjustments in timestamp to prevent drift.
- **Expansion Mode Operation** – Mode operation is not currently supported in the API or for Automated Test Script application.
- **Interrupting USB connection** – While PCIe Protocol Suite is running calibration do not unplug the USB cable as application may fault.
- **Gen2 Active Interposer** – Summit T3 and Summit T2 analyzers requires the original “Gen2 Active Interposer” x8 or x16 to be revision G or higher. The analyzers will not have a problem when operating with the new “Gen2 Active Interposer with SRIS”.
- **Updating Bus Engines** – When installing a new software release, a dialog may pop up asking to update FW and bus engines. After selecting OK the application will update the flash in the Summit T34 system and may prompt an error message. At this point, close the dialog, power cycle the Summit T34 system and after the system is ready the dialog asking to update FW and bus engines will pop up again. Select OK to proceed and the application will continue normally.
- **Known Problems and Issues Summit T2-16**
 - **External Reference Clock** – When External Reference Clock option is selected in Recording Options, Summit requires reference clock to be present at all times during the recording. Do not use this option for boot up recordings.
 - **Auto-Configure Lane Polarity** – for correct polarity detection Summit recording settings has to be initialized before the monitored link is trained. Switching between BitTracer and Protocol recordings is resetting all detected polarity settings.
 - **TLP Header with Payload match** – When both header and payload match values are used for trigger on TLP, analyzer may mistrigger on a condition when TLP with matching header and TLP with matching payload are coming back to back.
 - **Trigger Position** – Trigger positioning can be off by a few (up to 6) packets in either direction due to certain real-time constraints with heavy packet loading.
 - **EIOS errors** – Errors found in the EIOS packets as the system goes into low power state can be expected and are to be considered a normal part of operation
 - **Connectivity** – Summit T2-16 may not work correctly if connected over Ethernet and USB at the same time. Please use only one type of interface.
 - **Passive Interposer** – Summit T2-16 2 meter cable option will not work reliably Passive Interposer. Use 1-meter cables.
 - **Ethernet to USB switch** – After Summit T3 has been connected to the Ethernet in the PCIe Protocol

Suite application, user should go through the process of disconnecting the unit from the network before connecting it back using USB; otherwise the next recording taken may fault the application. This issue would be addressed in coming releases.

- **Gen2 Active Interposer** – Summit T3 and Summit T2 analyzers requires the original “Gen2 Active Interposer” x8 or x16 to be revision G or higher. The analyzers will not have a problem when operating with the new “Gen2 Active Interposer with SRIS”.
- **Known Problems and Issues PETracer Summit**
 - **External Reference Clock** – When External Reference Clock option is selected in Recording Options, Summit requires reference clock to be present at all times during the recording. Do not use this option for boot up recordings.
 - **Auto-Configure Lane Polarity** – for correct polarity detection Summit recording settings has to be initialized before the monitored link is trained. Switching between BitTracer and Protocol recordings is resetting all detected polarity settings.
 - **TLP Header with Payload match** – When both header and payload match values are used for trigger on TLP, analyzer may mistrigger on a condition when TLP with matching header and TLP with matching payload are coming back to back.
 - **EIOS errors** – Errors found in the EIOS packets as the system goes into low power state can be expected and are to be considered a normal part of operation
 - **Trigger Position** – Trigger positioning can be off by a few (up to 6) packets in either direction due to certain real-time constraints with heavy packet loading.
 - **Ethernet Connectivity** – Summit fails to work correctly if plugged directly into GigE network, works OK if plugged through 10/100 Ethernet HUB.
 - **USB support** – not supported
 - **Gen2 Active Interposer** – Summit T3 and Summit T2 analyzers requires the original “Gen2 Active Interposer” x8 or x16 to be revision G or higher. The analyzers will not have a problem when operating with the new “Gen2 Active Interposer with SRIS”.
- **Known Problems and Issues Summit T28**
 - **Auto-Configure Lane Polarity** – for correct polarity detection Summit recording settings has to be initialized before the monitored link is trained.
 - **EIOS errors** – Errors found in the EIOS packets as the system goes into low power state can be expected and are to be considered a normal part of operation
 - **Trigger Position** – Trigger positioning can be off by a few (up to 6) packets in either direction due to certain real-time constraints with heavy packet loading.
When setting a trigger such as "Enter Electrical Idle" or "Switch to ..." and the Trigger Position marker is set below 50% in the recording options dialog, the downstream direction may be missing traffic in the CATC Trace when there is a speed switch event from Gen1 to Gen2 or Gen3. This condition will be corrected in next releases.
 - **Short Electrical idle periods** – May not be detected by the analyzer and as a result Link Up Link down blocks may will not appear in the trace.
 - **Overclocking** – Only standard PCIe clocking is supported by the analyzers. No overclocking.
 - **Ignore Idle Errors Option** – Currently compliance patterns, shown as PATN packets in the CATC Tracer viewer, can only be filtered using the recording option “Ignore Idle Errors”. Using option “Ignore Idle Errors” may remove some training sequences just after link comes out of electrical idle.
 - **Auto Options** – Autospeed will not work properly for links already in low power states. Autoswizzle needs to see link training sequences (from configuration or recovery) in order to work. Also use fixed link width if the bus is in L0s state, this will be corrected in next releases.
 - **Low power states** – For L0s, the analyzer will filter out about 16 FTS due to lock, deskew and synchronization. If deskew is bypassed extra 3 to 4 FTS can be seen as being recovered only in bit tracer mode. During L0s low power traffic some LinkDwn/LinkUp packets might be missing from the trace when the electrical idle period is under 200ns. During L0s low power traffic some of the FTS packets may show with error marks. The PETracer tries to show as much data as possible after link comes out of electrical idle, this has the side effect of showing packets with errors until the analyzer has locked properly to the data. For L1, for devices that send less than 9 TS1s after exit of L1 these may not be recorded in the trace and sometimes deskew will be an issue if no SKP is part of the training sequence.
 - **Gen3 Options** – PETracer application shows Gen3 options as available for selection in some of the dialogs. T28 does not support these and they will be unavailable in future releases.
 - **EIOS** – When entering electrical idle during L0s, L1 or speed change, the Electrical Idle Ordered Set may be missing from the CATC trace in some recordings.
 - **Negative Time Stamps** – Precision of packet timestamp is 2ns and accuracy is measured as 3.75ns. Traces may show negative idle time periodically in the trace. This is due to minor periodic adjustments in

- timestamp to prevent drift.
 - **Buffer Size with Fixed LinkWidth** – When Link Width is selected as X1, X2 or X4 in recording options, the maximum buffer size available is half of the maximum size available for X8 or Auto.
 - **Current Firmware version report** – In Update dialog the T28 system may report it currently has version 0.00 for some Firmware files. In fact, all the required firmware files are up to date and the unit can be used for recording with no problem. This will be corrected in future releases.
 - **Gen2 Active Interposer** – Summit T3 and Summit T2 analyzers requires the original “Gen2 Active Interposer” x8 or x16 to be revision G or higher. The analyzers will not have a problem when operating with the new “Gen2 Active Interposer with SRIS”.
- Known Problems and Issues Summit T24
 - **Auto-Configure Lane Polarity** – for correct polarity detection Summit recording settings has to be initialized before the monitored link is trained.
 - **EIOS errors** – Errors found in the EIOS packets as the system goes into low power state can be expected and are to be considered a normal part of operation
 - **Trigger Position** – Trigger positioning can be off by a few (up to 6) packets in either direction due to certain real-time constraints with heavy packet loading.
 - **Short Electrical idle periods** may not be detected by the analyzer and as a result Link Up/Link Down blocks may not appear in the trace.
 - **Overclocking** – Only standard PCIe clocking is supported by the analyzers. No overclocking.
 - **Ignore Idle Errors Option** – Currently compliance patterns, shown as PATN packets in the CATC Tracer viewer, can only be filtered using the recording option “Ignore Idle Errors”. Using option “Ignore Idle Errors” may remove some training sequences just after link comes out of electrical idle.
 - **Auto Options** – Autospeed will not work properly for links already in low power states. Autoswizzle needs to see link training sequences (from configuration or recovery) in order to work. Also use fixed link width if the bus is in L0s state, this will be corrected in next releases.
 - **Low power states** – For L0s, the analyzer will filter out about 16 FTS due to lock, deskew and synchronization. If deskew is bypassed extra 3 to 4 FTS can be seen as being recovered only in bit tracer mode. During L0s low power traffic some LinkDwn/LinkUp packets might be missing from the trace when the electrical idle period is under 200ns. During L0s low power traffic some of the FTS packets may show with error marks. The PETracer tries to show as much data as possible after link comes out of electrical idle, this has the side effect of showing packets with errors until the analyzer has locked properly to the data. For L1, for devices that send less than 9 TS1s after exit of L1 these may not be recorded in the trace and sometimes deskew will be an issue if no SKP is part of the training sequence.
 - **Gen3 Options** – PETracer application shows Gen3 options as available for selection in some of the dialogs. T24 does not support these and they will be unavailable in future releases.
 - **EIOS** – When entering electrical idle during L0s, L1 or speed change, the Electrical Idle Ordered Set may be missing from the CATC trace in some recordings.
 - **Negative Time Stamps** – Precision of packet timestamp is 2ns and accuracy is measured as 3.75ns. Traces may show negative idle time periodically in the trace. This is due to minor periodic adjustments in timestamp to prevent drift.
 - **Gen2 Active Interposer** – Summit T3 and Summit T2 analyzers requires the original “Gen2 Active Interposer” x8 or x16 to be revision G or higher. The analyzers will not have a problem when operating with the new “Gen2 Active Interposer with SRIS”.
- Known Problems and Issues Summit Z3-16
 - **Wait=time** – Wait = time instruction may disable execution of subsequent wait instructions. To avoid this use wait on TLP with corresponding timeout.
 - **Bus engine update using USB HUB** – When updating the bus engine image while the host machine is connected to the Z3 through a USB HUB the PETracer application may keep waiting more than a minute for Z3 to restart. If this happens, simply unplug and plug back the USB cable. Another solution to this problem is to connect Z3 directly to the host machine USB port rather than through a HUB.
 - **Using Z3 with T3 Analyzer** – At 8 GT/s rate, Preset 7 is the default for TX Emphasis in the Generation Options for all lanes in the Summit Z3 Trainer. When using a Summit T3 to analyze the traffic, the DC Gain must be set to 3 dB in the Probe setting dialog on all the lanes for the traffic direction from the Z3 trainer. If you need to calibrate the link, select only the direction to be calibrated and skip the direction from the Z3 since that portion will be clean using the suggested DC Gain setting.
 - **ACK Latency** – Z3 will acknowledge a TLP after a minimum of 1 microsecond, this may more than the latency timeout for some devices in some configurations, a replay of the TLP may be generated by the DUT.
 - **Go to Link State drop down list** – When Emulation Role is selected as Device, the options Hot Reset and Disabled should not be used. This menu will change in future releases to make only valid options

- available per role selected.
- **Sending TLPs after Speed Change command when using scripts** – Add a command to wait for the link to be ready after a speed switch command and before transmitting any other packets.
- **PERST# Assertion** – This signal can be driven per user command in both Host and Device modes. When the Z3 Trainer is plugged in to a host system and in Device Mode, do not run a script to drive PERST#.
- **Script Automation Test Tool Root Complex Test Group** – Test 41-20 may fail for systems. This will be corrected in future releases.
- **NVME Drive Emulation in Windows7** – Read commands generated by NVME device emulation with the example driver are incomplete as shown by the CATC Trace. To correct this, change the trace decoding xml and edit the correct sizes for the IO completion and submission queues. Another way to correct this is to use Window 8.
- Known Problems and Issues Summit Z2-16
 - **Wait=time** – Wait = time instruction may disable execution of subsequent wait instructions. To avoid this use wait on TLP with corresponding timeout.
 - **Manual Ack/NAK mode** – operation of manual Ack/NAK mode is not reliable.
 - **Connection through USB HUB** – Summit T3 may require manual restart after BusEngine or Firmware update when it is connected to the host PC via USB Hub. Power the unit down and power it back up if software does not see the unit after the update.

3.4. API

Automation:

- There are several sample projects included with the application. To run them, you should make sure that the PCIe Protocol Suite application is already installed.
- The library file name to be used is: **PEAutomation.tlb**.
- **HTML Sample:**
- For editing the HTML sample code you can use a text editor or other HTML-editing tools.
- **CPP sample:**
 1. To compile the Visual C++ sample project you first need to install the Microsoft Visual Studio.
 2. You can select to install the PCIe Protocol Suite software in a directory different than the default one. If you choose to do so, make sure you add a path to the installation directory in your project's paths list. You can find the **PEAutomation.tlb** file in the installation directory. When using Microsoft Visual Studio, the path to the installation directory should be added to the following places in the 'Project Settings' property sheet:
 - (1) Under 'C/C++' tab, 'Preprocessor' category in the 'Additional include directories'.
 - (2) Under 'Resources' tab, in the 'Additional resource include directories'.
- When a remote server is disconnected abruptly while a client application is still connected, the client application might hang up for several minutes. At the end of this timeout, the client would be released with a "The RPC server unavailable" notification. This is due to Windows conduct. [C0004997]

4. Previous Releases

The following is a summary of previous releases of PCIe Protocol Suite and PETracer™:

4.1. Version 8.00

- PCIe Protocol Suite 8.0 version supports only Summit Z416.

4.2. Version 7.42/7.43

- MCTP over PCIe
 - New decoding level for MCTP Messages

- CATC View, Detail View, Spreadsheet view cells for MCTP Messages
- Basic error checking for MCTP Messages (packet presence, reserved fields, values validation)
- Traffic Summary report (Message Types, Control Command Codes, Control Completion Codes)
- Searches (Message Types, Control Command Code, Control Completion Code, Control Instance ID)
- MCTP Trigger template update
- VSE For MCTP messages
- Updates for NVMe 1.2 specification and improvements
 - Shared namespaces and controller id
 - ✓ Updated decoding
 - ✓ Added SSD base address dialog support
 - ✓ Updated command and payload cells
 - CATC Trace cells updates
 - ✓ Added and handled new SQE PSDT SGL codes, created proper cells for SGL descriptors.
 - ✓ Added new log pages, features, Identify CNS codes.
 - ✓ Added Controller List structure.
 - ✓ Now NVMe Transaction and Command levels use the same command specific and payload cells.
- Update to TCG decodes and added Opal decodes.
- Performance improvements for NVMe decodes and reports
- Search within NVMe Payload
- Search for NVMe LBA range
- IOPS utilization chart classified by device
- PMUX Decoding
 - New PMUX packet type and decodes for CATC Trace
 - Searches for PMUX on packet level
 - VSE support and examples
 - Error handling
 - Handling for TLP/PMUX short nullified packet
- Emulation of PCIe MCTP commands as host in Summit Z3
- Bug Fixes.

4.3. Version 7.40/7.41

- Decode for Multiple NVMe commands in single PCIe MemRd transfer
- Added search for NVMe command fields
- Graph NVMe queue depths
- NVMe controller map display
- Adding PERST signal to PETracer, display, triggering, searches
- Support for high efficiency memory for T34 expansion mode
- Enhancement to trace export
- Enhancements to decoding: Check validity of Vendor Specific Command Configuration
- GUI improvements : Sorting bookmarks by timestamp
- PCIe Vendor Defined Messages with MCTP content : Decodes, Triggers, Searches, VSE
- Result summary list for ATS
- Enhancements to Summit Z3 scripts : Enhanced control for TD bit
- NVMe 1.2 specification commands for Z3 NVMe Host Controller emulation
- NVMe 1.2 specification update for Z3 Drive emulation
- Bug Fixes.

4.4. Version 7.35

- Bug Fixes.

4.5. Version 7.34

- Added T34 memory high efficiency mode for X1 and X2
- Enabled T34 expansion mode for X8 recording
- PCIe ECN update : Precision Time Measurement
- New USB driver for T34
- New Trace Expert feature for NVMe
- New NVMe mode for long recording
- Added per lane triggering to analyzers
- Modified selection for LinkWidth recording option in analyzer
- Enabling license keys for TCG decoding
- Miscellaneous Enhancements for NVMe and Storage Decodes
- Bug Fixes.

4.6. Version 7.30

- T34 Support
- X34 Support
- New high level decode for NVMe Transactions
- TCG Decodes for NVMe: TCG Enterprise SSC Version 1.0 Revision 0.9
- Traffic Summaries for NVMe, AHCI, ATA
- Error Reports for NVMe, AHCI, ATA
- VSE for NVMe, AHCI, ATA
- Decoding for multiple NVMe devices in the same bus
- New capabilities for spreadsheet view for improved usability
- Triggers added to analyzers: LinkWidth Change, Compliance Pattern.
- Triggers and filters added to analyzers: CLKREQ#, WAKE#.
- Update Z3 Trainer NVMe drive emulation to 1.1a specification adding Get Log Page, Security Send and Security Receive Admin, Dataset Management Commands
- New Z3 Trainer capabilities : Freeze flow control, insert TLP Prefixes types, disable Power Management DLLPs auto mode, TD bit corruption
- Bug Fixes.

4.7. Version 7.20

- Gen3 Compliance Package for Z3.
- Support for Large Traces.
- Update for Z3 NVMe drive emulation.
- Bug Fixes.

4.8. Version 7.10

- Updates for storage protocol specifications in display, search, and triggers.
- SCSI command decodes to trace view.
- Spreadsheet view for PQI/SOP control registers.
- Spreadsheet view for SCSI commands.
- Trigger Templates for PQI/SOP control registers.

- Trigger Templates for PQI/SOP/SCSI commands.
- Search for PQI/SOP control registers.
- Search for SCSI commands.
- Z3 Host emulation scripts PQI/SOP control registers.
- Z3 Host emulation scripts SCSI commands.
- Added SRIS Support to Z3.
- OBFF decode based on WAKE#.
- Bug Fixes.

4.9. Version 7.00

- Spreadsheet view supporting storage decodes for NVMe, AHCI and ATA.
- Search functionality for NVMe and ATA commands in decoded traces.
- Trigger options for NVMe, AHCI and ATA commands.
- Added support for CLKREQ# for Summit Analyzers.
- Option ROM capability added to Z3 Trainer.
- Storage scripts for Z3 Trainer NVMe and AHCI structures.
- Added support for new Summit Z3 with CLKREQ# support.
- Bug fixes.

4.10. Version 6.75

- New functionality added to Summit T3-16, T3-8 and T28: Five DW floating payload match.
- PQI 6i decode for CATC Trace viewer.
- SATA 2.6 decode for CATC Trace viewer.
- Improvements to Automated Test Script.
- Enhancements to API.
- Bug fixes.
- Last release supporting Edge, ML and EML products.

4.11. Version 6.71

- AHCI 1.3 decoding improvements.
- PQI dialog box updates.
- PQI decoding: XML Export/Import.
- AHCI Sample scripts for Summit Z3.
- Bug fixes.

4.12. Version 6.70

- Added support for Summit T24 analyzer.

4.13. Version 6.65

- Improved memory efficiency for Summit T3 recordings.
- SSD protocol decoding improvements.
- Bug fixes.

4.14. Version 6.63

- Improved memory efficiency for x1, x2, x4 recordings for Summit T3-16.
- NVMe protocol decoding improvements.

4.15. Version 6.60

- AHCI 1.3 decoding support.
- PQI decoding support.

- Dashboard view is enhanced with additional link information.
- CATC Walk support added.
- Summit T3 calibration procedure improvements.
- Host memory spaces support for Summit Z3.
- Summit Z3 NVMe device emulation support added.

4.16. Version 6.53

- Dashboard functionality for Summit T3-8, Summit T3-16 and Summit T28.
- Real Time Statistics display is fixed.

4.17. Version 6.51

- Expansion Recording support for Summit T3-8 is fixed
- Real Time Statistics display is fixed

4.18. Version 6.50

- Added support for CrossSync in Summit T28
- The software application now supports the connectivity of multiple LeCroy PCIe analyzers
- Added support for L0s/L1 LTSSM states on Summit Z3
- Added support for Summit T28 calibration

4.19. Version 6.45

- Support for Real Time Statistics (RTS) on Summit T3-8/T3-16
- Support for CrossSync on Summit T3-8/T3-16 (restricted to operation with other LeCroy analyzer, but not additional PCIe products)
- Support for ATS (Address Translation Services)

4.20. Version 6.40

- Summit T28 Analyzer support
- Summit T3-8 Expansion support

4.21. Version 6.30

- Summit T3-8 Analyzer support
- Summit Z3-16 Exerciser full gen3 support

4.22. Version 6.23

- Summit T3 support for AutoSwizzling
- NVMHCI transaction decoding
- Summit Z3 support for script based generation at gen1 and gen2 speeds
- New Summit Z3 LTSSM control dialog
- Bug fixes

4.23. Version 6.2

- Summit T3 Advanced Triggering options added.
- Bug Fixes

4.24. Version 6.1

- Added support for Summit Z3-16 PCI Express exerciser product.

4.25. Version 6.0

- Added support for Summit T3-16 PCI Express analyzer product.

4.26. Version 5.73

- Bug Fixes

4.27. Version 5.72

- ProtoSync support added. ProtoSync allows PETracer to import data from LeCroy scopes and synchronize protocol views with the scope waveform.
- TraceView packet selection implemented.
- Bug Fixes

4.28. Version 5.71

- Added support for Atomic Operations
- Added support for TLP Processing Hints
- BitTracer functionality is enhanced with speed information, Ordered Set color coding and packet statistics
- Added support for PCI Express 3.0 specification packet decoding
- Bug Fixes

4.29. Version 5.70

- Configuration space decoding is compliant with PCI Express specification 2.1
- Bug Fixes

4.30. Version 5.62

- Summit trigger on MR-IOV TLP packets support added
- Summit traffic based RealTime Statistics graphs added
- Summit x8 filtering issues are fixed
- Bug fixes

4.31. Version 5.61

- Summit T2-16 USB problems are fixed.
- Summit T2-16 and Summit Auto Link Width tracking support added.
- Summit T2-16 and Summit ability to record low power states and speed switch transitions improved.
- Summit T2-16 and Summit start tracking lane polarity inversion right after power on, without a requirement of being configured to do so by the software.
- Summit T2-16 and Summit false Trigger on Errors are fixed.
- Auto Update feature included, PETracer software can be configured to check for updated version automatically.
- Summit Z2-16 support for multi-TLP waits added
- Summit Z2-16 number of supported branches increased to 32.
- Summit Z2-16 scripting extension to support arithmetic operations with counter variable within the Repeat block for the Address Space Offset parameter of the Field/Payload Substitution construct

4.32. Version 5.6

- Summit Z2-16 Fast Transmit functionality added
- Summit Z2-16 Memory Completer functionality added
- Summit Z2-16 Ethernet support added
- Multiple Summit Z2-16 bug fixes
- PETracer Summit and Summit T2-16 stability improvements
- Edge T1-4 support added.
- MR_IOV Trace view decoding added. Please note that other views are not updated and may display incorrect information for MR-IOV packets.

4.33. Version 5.5

- Summit Z2-16 Gen 2 exerciser released.

4.34. Version 5.4

- Added support for Summit T2-16 analyzer.

4.35. *Version 5.3*

- PETracer Summit support of Automatic receiver Calibration to ensure error free recordings is introduced.
- DataFlow View is added as an additional method of analyzing PCI commands.
- Bug fixes

4.36. *Version 5.21*

- Maintenance expiration check in PETracer 5.2 is fixed.

4.37. *Version 5.20*

- Summit Bit Tracer recording is released
 - Logic Analyzer style time based view
 - Simple triggering on 10bit symbol
 - Embedded packet view
 - Apply different Link parameters to the view (lane skew, link width, polarity, lane swizzling, scrambling settings)
 - Export to CATC trace
- Compact view functionality added
- LTSSM Flow graph functionality added

4.38. *Version 5.10*

- PETracer Summit support
 - Gen1/Gen2 recordings at x1, x2, x4, x8 and x16 lane widths
 - 8GB Recording Buffer
 - MidBus Probe support
 - Ethernet connectivity

5. Support

Online Download

Please periodically check Teledyne LeCroy Protocol Solutions Group's web site for software updates and other support related to this product. Software updates are available to those users with current Maintenance Agreements.

Online Support

Web: <http://teledynelecroy.com/support/contact/>

Web (SW downloads): <http://teledynelecroy.com/support/softwaredownload>

E-Mail: <mailto:psgsupport@teledynelecroy.com>

Phone Support

Voice: +1 800 909 7112 (USA/Canada)

+1 408 653 1260 (worldwide)

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