

PCI Express® 6.0 M.2 Interposer



Key Features

- Quick and simple set-up
- Allows capturing and decoding of all PCI Express traffic between host and device
- Supports NVMe Express (NVMe) and SATA Express (ATA/AHCI/PCIe)
- Supports "M/B-M" key memory SSD
- Supports SSD sizes of 30mm, 42mm, 60mm, 80mm and 110mm (all x 30mm width)
- Supports data rates of 2.5 GT/s - 64GT/s
- Supports link widths of x1, x2 and x4
- Independent clock selection per traffic direction
- Supports SRIS clocking
- Supports CLKREQ# for testing of L1 substates
- Supports SMBus
- Environmental:
 - Temperature (operating): 5° to 40°C (41° to 104°F)
 - Temperature (non-operating): -20° to 60°C (-4° to 140° F)
 - Humidity (operating): 5% to 80% RH (non-condensing) at <=30°C, 50% max RH (non-condensing) at 40°C
 - Humidity (non-operating): 5% to 95% max RH (non-condensing)
- Dimensions:
 - 210mm x 166mm x 46mm (8.27" x 6.54" x 1.81")
- Weight:
 - .98lbs
- Power:
 - 12V 60W AC/DC

Used with Summit™ Protocol Analyzers, the PCIe 6.0 M.2 Interposer enables PCIe bus traffic between a system board or tablet and an M.2 connector on a SSD device to be monitored, captured, and recorded for protocol analysis. The M.2 interposer will support analysis of PCI Express 6.0 and NVMe Express (NVMe) Protocols at data rates from 2.5 GT/s up to 64.0 GT/s, and link widths of x1, x2 and x4.

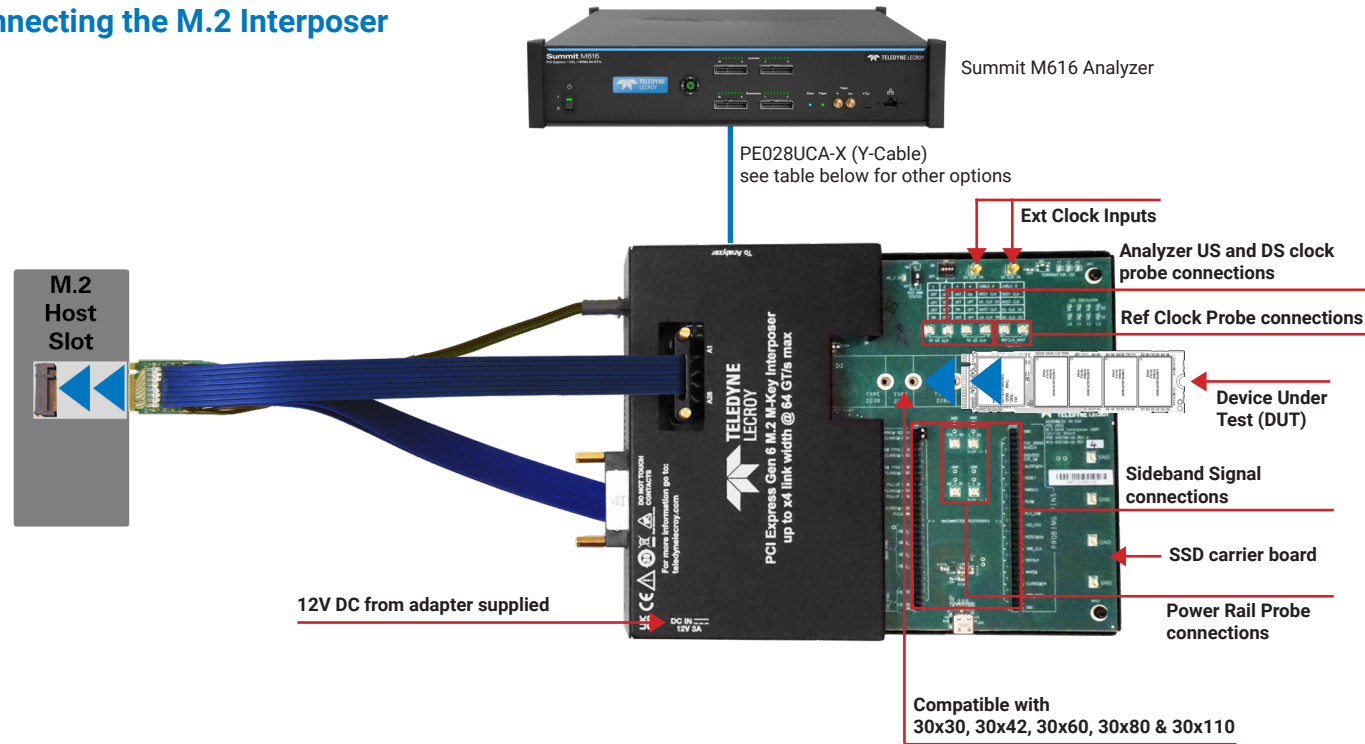
M.2 is a mechanical standard that is very commonly used for PCI Express based storage devices that support the NVMe protocol, in addition to legacy SATA Express based drives. The M.2 Interposer allows developers of M.2 based devices quick and convenient access to analysis of the protocol behaviors of the PCI Express link. Together with a Summit series Protocol Analyzer, and the Teledyne LeCroy PCI Express Protocol Suite set of software tools, the M.2 Interposer is the key to capturing and analyzing integrity and performance of the devices under test.

The PCIe 6.0 M.2 interposer allows testing of M.2 devices with heat sinks or other attached fan devices. The PCIe 6.0 M.2 Interposer also probes the sideband signals, including #PERST, #CLKREQ and #WAKE, allowing analysis of low-power states in PCI Express, including L1 substates. Probe points for the sideband signals are also provided on the interposer, providing a convenient way to connect and observe using an Oscilloscope. The interposer has its own power supply and does not draw any power from the link being monitored.

The Multiport feature on Summit Protocol Analyzers enables more than one link to be monitored simultaneously, including bifurcated or independent links.



Connecting the M.2 Interposer



PCIe 6.0 M.2 Interposer Analyzer and Cable Compatibility		
	Analyzer Platform	Cable Requirement(s)
	Summit T48 Protocol Analyzer (Standard)	PE031UCA-X (Y-Cable)
	Summit T48 Protocol Analyzer (Multiport)	PE030UCA-X (Straight Cable)
	Summit T416 Protocol Analyzer (Standard)	PE031UCA-X (Y-Cable)
	Summit T416 Protocol Analyzer (Multiport)	PE030UCA-X (Straight Cable)
	Summit T54 Protocol Analyzer (G5 PHY)	PE027UCA-X (Straight Cable)
	Summit M5x Protocol Analyzer	PE029UCA-X (Y-Cable)
	Summit T516 Protocol Analyzer (Standard)	PE028UCA-X (Y-Cable)
	Summit T516 Protocol Analyzer (Multiport)	PE027UCA-X (Straight Cable)
	Summit M616 Protocol Analyzer (Standard)	PE028UCA-X (Y-Cable)
	Summit M616 Protocol Analyzer (Multiport)	PE027UCA-X (Straight Cable)

Ordering Information

Product Description
PCI Express 6.0 x4, M.2 M-Key Interposer
PCI Express 6.0 M.2 Interposer Probe Cable

Product Code
PE6204UIA-X
PE6204ACA-X

Local sales offices are located throughout the world.
Visit our website to find the most convenient location.
1-800-5-LeCroy • teledynelecroy.com

