Low-speed Serial Data Trigger, Decode, Measure/Graph, and Eye Diagrams

Key Features

More than 20 supported standards:
- I²C
- SPI, SPI DDR, Simplified SPI
- UART and RS-232
- USB 1.x, 2.0 and USB 2.0 HSIC
- CAN and CAN FD
- LIN
- FlexRay
- SENT
- ARINC 429
- MIL-STD-1553
- I²C
- SPMI
- SPACEWIRE
- 10/100 Base-T Ethernet, EtherCAT
- DPHY
- DigRF 3G, DigRF v4
- I²S, incl. LJ, RJ, TDM
- Manchester
- NRZ

Most powerful, flexible triggering capabilities

Intuitive, color-coded decode overlays

Single protocol results table supports up to 4 decoders at one time

Unique measure/graph capabilities:
- Automated timing measurements
- Serial DAC - extract digital data and plot it as a waveform
- Bus parameters

Physical layer eye diagrams

Teledyne LeCroy’s Trigger (T), Decode (D), Measure/Graph (M or G) and Eye Diagram and Physical Layer (E or P) options are the best in the industry and nearly universally available across the entire Teledyne LeCroy oscilloscope product line.

Highest Performance Triggers
Designed by people who know the standards, with the unique capabilities you want to isolate unusual events. Conditional data triggering permits maximum flexibility, and highly adaptable error frame triggering is available to isolate error conditions. Frame definition allows grouping of UART or SPI packets into message frames for customization.

The Best Serial Decoder
Decoded protocol information is color-coded to specific portions of the serial data waveform and transparently overlaid for an intuitive, easy-to-understand visual record. All decoded protocols are displayed in a single time-interleaved table. Touch a row in the interactive table to quickly zoom to a packet of interest and easily search through long records for specific protocol events using the built-in search feature.

Measure/Graph Tools for Validation Efficiency
Quickly validate cause and effect with automated timing measurements to or from an analog signal or another serial message. Make multiple measurements in a single long acquisition to quickly acquire statistics during corner-case testing. Serial (digital) data can be extracted to an analog value and graphed to monitor system performance over time, as if it was probed directly. Complete validation faster and gain better insight.

Eye Diagrams & Physical Layer
Rapidly display an eye diagram of your packetized low-speed serial data signal without additional setup time. Use eye parameters to quantify system performance and apply a standard or custom mask to identify anomalies. Mask failures can be indicated and can force the scope into Stop mode.
Every serial trigger we design exhibits deep knowledge of the standard. Most serial triggers work with digital (MSO) inputs, or the EXT input for the Clock line so as to conserve analog channels. Each serial trigger has some unique aspect for high performance, such as:

- **I2C trigger permits triggering on data in a specific location of an up to 2048 byte I2C EEPROM read or write.**
- **UART or SPI bytes can be combined into a single “message frame” - trigger on custom protocols based on UART or SPI byte blocks.**
- **UART supports 9-bit “address” or “wakeup” mode triggering.**
- **CAN, CAN FD, LIN, FlexRay and MIL-STD-1553 permit conditional ID/Address triggering.**
- **CAN and CAN FD permit triggering symbolically using a DBC file.**
- **USB 2.0 and MIL-STD-1553 triggers permits complex transaction definition and triggering.**

### Conditional DATA Setup

Every Teledyne LeCroy low-speed serial trigger that incorporates DATA trigger permits a conditional (<, <=, =, >, >=, <>), inside a range, outside a range) setup for the DATA condition. This is especially useful in situations where abnormal events should be monitored, such as when a temperature sensor transmitting via I2C exceeds a maximum temperature, or a CAN node broadcasts a low or high engine RPM or coolant pressure. Furthermore, data for triggering can be specifically isolated in very long byte streams to specific bit locations, even those which span data bytes.

### Support for Many Proprietary Protocols

Many proprietary serial protocols make use of the common UART (single Data line) or USART (Clock and Data lines, e.g., SPI) byte structures, with multiple bytes grouped into proprietary protocol definitions. Our highly flexible UART byte and SPI format definitions accommodate nearly any customer need, and the UART or SPI bytes can be defined to be part of a single “message frame” through use of our Interframe Setup. Then, the trigger pattern setup can isolate any byte value, e.g., an ID, or a DATA string value, that is part of your proprietary protocol message definition.
SIMPLY THE BEST SERIAL DECODER

Our serial decode, search and table tools work exactly the way you want. These tools are the industry standard for turning your oscilloscope into a protocol analyzer with fast and intuitive correlation of protocol data to the physical layer waveforms.

Intuitive, Color-Coded Overlays
A transparent overlay with color-coding for specific portions of each protocol and the entire message frame makes it easy to understand your serial data information. Unlike other solutions, with protocol decode information away from the signal, our solution correlates the waveform and the protocol decode directly on the display. As the acquisition length is expanded or shortened, the decode overlay will adjust to show you just the right amount of information.

Pattern Search
All decoders provide ability to search through a long record of decoded data by using a variety of search criteria, or values, or simply finding the next occurrence. Pattern Search automatically creates a zoom trace of the acquired waveform and displays the selected location complete with the transparent color-coded overlay.

Interactive Table Summarizes Results
Turn the oscilloscope into a protocol analyzer with a tabular display of decoded information. Customize the table to show only the data of interest and touch a message in the table to automatically zoom to it and display it on the screen. Export the table for offline analysis. Up to four different decoded signals of any type may be simultaneously displayed in the table.
Key Features

Timing measurements
- Serial Message to Analog Signal
- Analog Signal to Serial Message
- Serial Message to Serial Message

Serial DAC measurement/graphing

Bus status measurements

Automated – quickly gather statistics, display Histograms

Quickly correlate cause-effect timing relationships to other events

Conditional filtering

Supported for
- I2C
- SPI, SPI DDR, Simplified SPI, Custom SPI
- UART
- RS-232
- USB 1.x and 2.0
- CAN
- CAN FD
- LIN
- FlexRay
- ARINC 429
- MIL-STD-1553
- I2C
- SPMI
- DigRF 3G
- DigRF v4
- I2S, incl. LJ, RJ, TDM (note: Serial DAC graph, or "G" capability only)

The measurement and graphing capabilities significantly enhance our trigger and decode packages, and help you debug and validate faster.

Digital data can be extracted and rescaled to an analog value and graphed over time, time-correlated to other acquired data, as if you had probed it directly. It’s a Serial Data DAC!

Automated cause-effect timing measurements can be made between analog signals and serial data messages, or two serial data messages. Use with serial triggering and long acquisitions to understand system behavior during stress or corner-case testing. A variety of bus status measurements are also available.

All measurements may be used with the rich set of standard Teledyne LeCroy standard parameter analysis tools, including automated pass/fail analysis with boolean test conditions, measurement gates, measurement accept, filtering, parameter math, and custom math.

Setup is easy in the Measure/Graph setup tab:
1. Choose the source
2. Choose the measurement
3. Select the destination parameter (e.g., P2)
4. Then apply and configure
Serial Data DAC and Graphing Tools

Digital data can be extracted from specific locations in the serial data message using the Message to Value measurement parameter - a serial data DAC. This information can then be displayed as a measurement parameter value(s), or it can be viewed as a time-correlated waveform displaying the measurement value over time - as if you were able to probe and acquire it directly. Use the long acquisition time of the oscilloscope to understand how the data changes over long periods of time, in conjunction with other system behaviors.

Some examples of the usefulness of this capability are:

- Viewing I²C or SPI temperature sensor data
- Viewing DigRF 3G radio frequency I and Q modulated signals
- Viewing CAN wheel speed information used by an ABS
- Viewing reconstructed analog audio from serial I²S streams

Shown above is a long acquisition of a CAN serial data signal (top waveform) that contains embedded digital data for steering wheel angle rate of change (deg/s, or Hz). The Message to Value parameter was configured to locate and extract the digital steering wheel angle range data from particular locations in specific CAN serial messages, and then converted from digital to analog form with proper re-scaling and physical units. The serial data DAC waveform (bottom waveform) is shown in the lower grid.
Automated Timing Measurements

Utilize a serial trigger to isolate a specific message and then measure a cause-effect timing relationship with a subsequent analog signal, or vice versa. But instead of manually measuring the timing with cursors, use these tools to automate the measurement and return thousands of values quickly as your system undergoes stress testing. Automate the measurement and validation of gateway latency times from one serial message to another (e.g. CAN to LIN or low-speed CAN to high-speed CAN, or CAN to FlexRay) without having to manually use cursors or compare values and times in a protocol table. Quickly understand bus latency times or arbitration behaviors by measuring the difference between two messages on a single decoded waveform. Dramatically improve your validation efficiency and time to insight.

**Analog to Message**
Computes the time difference from a protocol message meeting specified conditions to the crossing of a threshold on an analog signal.

**Message to Message**
Computes the time difference from a protocol message meeting specified conditions to another protocol message meeting specified conditions.

**Message to Analog**
Computes the time difference from a protocol message meeting specified conditions to the crossing of a threshold on an analog signal.

**Delta Message Time**
Computes the time difference between two messages on a single decoded line.

**Time@Message**
Time from Trigger to each protocol message meeting specified conditions.

Use the Message to Analog measurement to find the time between an I2C data packet and a control signal on another channel. Multiple measurements in one or more triggers could be made to understand behaviors over time or under different operation conditions.
**Bus Status Measurements**

The bus status measurements Bus Load, Message Bitrate, and Number of Messages, give an overall status of the decode protocol to quickly learn if the bus is over utilized and to verify the bit rate matches expectations.

**The Perfect Oscilloscopes for the TDME Options**

Teledyne LeCroy HDO, WavePro HD, and WaveRunner oscilloscopes are the perfect oscilloscope platforms to utilize the TD and TDME toolsets.

Teledyne LeCroy’s 12-bit Hgh Definition Oscilloscopes (HDOs) provide 12-bit resolution and either 4 or 8 analog input channels up to 8 GHz with MSO digital input options. These oscilloscopes are have powerful standard toolsets for debugging deeply embedded designs with analog, digital, serial data, and sensor signals. Their 12-bit resolution is ideal for measuring sensor signals and correlating them to other system activities. 8 analog input channels provides more ability to correlate more signals to each other.

Teledyne LeCroy WaveRunner oscilloscopes, such as the WaveRunner 9000 Series, are also extensively used for embedded system debug. Their standard toolsets complement the TDME packages extremely well.
Eye Diagrams are “bit-sliced” views of the physical layer serial data waveforms. They provide a fast, intuitive way to understand physical layer signal integrity. Eye Diagrams may be combined with masks and mask failure indications, and eye (opening) parameters. Protocols with challenging topologies (e.g. FlexRay) provide even more advanced measurement capabilities.

**Key Features**

- Up to four simultaneous Eye Diagrams
- Simple to set up - one button push
- Include standard or custom masks. Create your own masks.
- Eye parameters
- Mask failure indication
- Failure locator trace waveform
- Pass/Fail with STOP on failure

Eye diagrams “slice” each bit and overlay them to get a consolidated view of signal quality. Intrusions into the eye opening or onto a mask indicate potential problems.

**Up to 4 Simultaneous Eye Diagrams**

Up to four serial data signals can be decoded and displayed as eye diagrams at one time. These can be different protocols, or the same protocol measured at different points (e.g., transmit and receive, different nodes, or different standard-defined test points). Apply a user-defined filter to each eye diagram to only display specific signals in the eye.

**Eye Diagram Measurement Parameters**

Quantify physical layer signal quality in the eye by applying parameters for Eye Height, Eye Width, and Number of Mask Failures. Some packages (e.g. FlexRay TDMP) go a step further and include additional measurements defined in the standard.
Mask and Mask Failure Indication

A user-defined or pre-defined mask may be added to the eye diagram so as to objectively evaluate if the physical layer signal intrudes too far into the eye opening. Apply a filter to include or exclude specific messages from the Eye so as to determine failure source (e.g., messages from a specific node or with a specific ID). Mask failures are indicated with a red circle and can be displayed in a table. Touch the failure table to open a zoom of the failed area for further inspection.

Some standards, due to their speed or nodal complexity, provide specific guidance on what eye diagrams or measurements should be made and exactly how they should be performed. FlexRay, and MIPI DPHY are examples. In these cases, the Eye Diagram ("E") capability is augmented with additional specialized "P" capability (for Physical Layer Measurements), per the standard. In these cases, the "E" capabilities previously described are also available.
**Key Features**

Set an ACK condition (ACK, NO ACK, Don’t Care) in all frame trigger setups

Does not require clock trace to be displayed during decode

EEPROM read/write 2048 byte trigger capability

Frame Length trigger capability

Address can include a R/W bit, or define as Don’t Care

Use analog or digital (MSO) inputs for acquisition and triggering.

EXT input may be used for clock signal

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**More Trigger Choices**

In addition to typical Start/Stop/ReStart, NoAck, Address and Address+Data triggers, Teledyne LeCroy provides triggering for EEPROM read/writes up to 2048 bytes long and for Frame Length. Address-based triggers permit an additional ACK condition (ACK present, NO ACK present, or DON’T CARE), and selection to include a R/W bit in a 7-bit trigger.

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**More Flexibility for Address-based Triggers**

Address-based triggers permit an additional ACK condition (ACK present, NO ACK present, or DON’T CARE), and selection to define the transfer direction as a READ, WRITE or DON’T CARE (using R/W bit in a 7-bit trigger, or R/W Direction selection in a 10-bit trigger).
### I2Cbus TD and I2Cbus TDME

**Definition**

**Source Setup**
Select Source for Clock and Data.

**Trigger Capability**

**Format**
Hexadecimal or Binary. ADDRESS and DATA can be set up with different formats.

**Trigger Setup**
Trigger on START, ReSTART, STOP, Missing ACK, ADDR, DATA, ADDR+DATA, ADDR+DATA FRAME LENGTH, EEPROM DATA TRANSFER

**ADDRESS Setup**
Specify one ADDRESS with condition of ".=".
7 or 10 bit ADDRESS supported with full Read, Write, or R/W="Don't Care" selectability on both 7 and 10 bit ADDRESSes. Choose to Trigger on address values that include/don't include R/W bit in address value.

**DATA Setup**
ADDRESS+DATA Trigger Type:
Hexadecimal: # Data Bytes = 0 to 12. Data can be defined by nibble.
Binary: Any combination of 0,1, or X for 1-96 bits
Data pattern can be set to start at any location in the 12 Byte / 96 bit sequence.
EEPROM DATA TRANSFER Trigger Type:
# Data Bytes = 0 to 2047.
Data pattern can be set to start at the beginning of any byte in an up to 2048 byte sequence.

**DATA Conditions**
<=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE.

**ACK Conditions**
For any ADDR, ADDR+DATA, ADDR+DATA FRAME LENGTH, or EEPROM DATA TRANSFER setup, select an ACK Condition of ACK, NO ACK, and DON'T CARE.

**Bit Rates**

**Trigger Input**
Any analog Channel or Digital input, or the EXT input. Clock may be input to EXT to conserve available analog Channels.

**Decode + Search Capability**

**Format**
Hexadecimal, Binary, ASCII.

**Decode Setup**
Threshold definition required. Default is to Percent amplitude. Choose to Decode address values including/not including the R/W bit in address value.

**Decode Input**
Any analog Channel, Memory or Math trace, and any Digital trace.

**# of Decodes**
Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).

**Visual Aid**
Color Coding for FRAME, START/ReSTART bit, ADDR, R/W, DATA, ACK, NACK, and STOP bit.
Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.

**Table Configure, Export Table**
Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.

**Pattern Search**
Search for Previous or Next ADDRESS, PACKET, or DATA in hexadecimal format.

### I2Cbus TDME only

**Measure / Graph Capability**

**Serial Data Digital-to-Analog Conversion (DAC)**
Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion.

**Serial DAC Waveform**
plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.

**Timing Measurements**
Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Time@Message (time from trigger) measurement parameters.
Serial Message may be defined by "ID = " (where applicable) and user-defined DATA condition of <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.

**Bus Status Measurements**
Number of Messages, Message Bit Rate, Message Bus Load %
Serial Message may be defined by "ID = " (where applicable) and user-defined "DATA <=, <, =, >, >=, <>, in range, out of range" in any location in up to 2048 bits of data.

**Eye Diagram Capability**

**Setup**
Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.

**Eye Parameters**
Eye Height, Eye Width, (Number of) Mask Hits

**Eye Mask**
Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.

**Failure Indication and Location**
Mask Failure Indication ON or OFF (ON = indicated with a red circle).
Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.
Key Features

Supports nearly any type of SPI, including simplified SPI with no Chip Select and SPI DDR

Interframe message time setup permits Frame definition for support of many proprietary USART-based protocols

Flexible Bits/Word Decode Setup

Does not require clock trace to be displayed during decode

Use analog or digital (MSO) inputs for acquisition and triggering.

EXT input may be used for clock signal

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SPI Triggering When No Chip Select is Present

Most SPI triggers require that a Chip Select signal be present. However, simplified (single-master, single-slave) SPI (also referred to as SSPI or SIOP) has no Chip Select. Our solution still permits triggering on simplified SPI through use of an Interframe Setup time. In most cases, the AUTO default provides accurate results, but MANUAL selection is also available.

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Interframe Message Time Setup for Proprietary USART-based Protocols

Two line (CLOCK and DATA) serial data signals with proprietary formats are sometimes used. This is commonly known as a USART. Given the flexibility of the setup of our trigger and decoder, it is often possible to use the Interframe Message Time Setup to “packetize” consecutive bytes into one Message Frame, and then trigger on serial data in a particular byte location in the complete multi-byte Frame. Then, SPI-CUSTOM may be used to decode information as a complete Frame instead of individual Bytes.
## SPECIFICATIONS

### SPIbus TD and SPIbus TDME

**Definition**

*SPIbus TD and SPIbus TDME* select Source for Clock, Data, and Chip/Slave Select (Chip/Slave Select not required for SIOP or SSPI types).

**Source and Protocol Setup**

Select SPI Type (SPI, SIOP, SSPI, or SPI-DDR). SPI Type CUSTOM is also available in the decoder. For SPI or SPI-Custom, select CPOL (Clock Polarity 0 or 1) and CPHA (Data Polarity 0 or 1) (SIOP permits CPOL selection of 0 or 1, but CPHA =1; SSPI CPOL=1, CPHA=1; SPI-DDR does not have CPOL or CPHA selection). Select DATA = MSB or LSB.

**Trigger Capability**

**Format**

Hexadecimal or Binary

**Trigger Setup**

Trigger on DATA for any of the five SPI Modes with either MSB/LSB and with or without Slave Select.

**DATA Setup**

Hexadecimal: # Data Bytes = 0 to 12. Data can be defined by nibble. Binary: Any combination of 0, 1, or X for 1-96 bits

**DATA Condition Setup**

<, <=, >, >=, !=, IN RANGE, OUT OF RANGE, or DON'T CARE

**Bit Rates**

Any, up to 25 Mb/s (typical). Auto-detected from clock signal.

**Trigger Input**

Any analog Channel or Digital input, or the EXT input. Clock or Chip/Slave Select may be input to EXT to conserve available analog Channels.

**Decode + Search Capability**

**Format**

Hexadecimal, Binary, ASCII

**Decode Setup**

Threshold definition required. Default is to Percent amplitude. Choose to Decode address values including/not including the R/W bit in address value.

**Decode Input**

Any analog Channel, Memory or Math trace, and any Digital trace. Clock channel may be turned OFF and data will still decode (reduces screen clutter)

**# of Decoded Waves**

Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).

**Visual Aid**

Color Coding for FRAME, START/RESTART bit, ADDR, R/W, DATA, ACK, NACK, and STOP bit. Decode information is intelligently annotated based on timebase setting.

**Table Configure, Export Table**

Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displays information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to _csv file, and special display of long data or other patterns.

**Pattern Search**

Search for Previous or Next MESSAGE or DATA Pattern in hexadecimal format.

### SPIbus TDME only

**Measure / Graph Capability**

**Serial Data Digital-to-Analog Conversion (DAC)**

*Message to Value* measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion.

**Serial DAC Waveform** plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.

**Timing Measurements**

*Message to Analog, Analog to Message, Message to Message, ΔMessage Time* (identical message on same decoder), *Time@Message (time from trigger)*

Serial Message may be defined by “ID =” (where applicable) and user-defined DATA with condition <=, <, =, >, >=, !=, IN RANGE, OUT OF RANGE in any location in up to 2048 bits of data.

Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.

**Bus Status Measurements**

*Number of Messages, Message Bit Rate, Message Bus Load %*

Serial Message may be defined by “ID =” (where applicable) and user-defined “DATA <=, <, =, >, >=, !=, IN RANGE, OUT OF RANGE” in any location in up to 2048 bits of data.

**Eye Diagram Capability**

**Setup**

Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.

**Eye Parameters**

Eye Height, Eye Width, (Number of) Mask Hits

**Eye Mask**

Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.

**Failure Indication and Location**

Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.
### Key Features

Completely configurable UART-byte structure

Customizable Message Frame (multiple bytes in one Frame) for proprietary protocol triggering

Supports 9-bit “address” or “wakeup” mode in byte definition (triggering and decoding)

Supports up to 16-bit Data words for decoding

Binary, Hexadecimal, ASCII or Decimal decoding

Polarity either IdleLow or IdleHigh

Use analog or digital (MSO) inputs for acquisition and triggering.

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9-bit “Address” or “Wakeup” Triggering

Most UART triggers assume a maximum of 8 data bits (excluding stop/start and parity bits) in a single byte. However, our solution supports 9-bit data bytes for situations in which a UART protocol is utilized for Address, Wakeup or other communication to another peripheral, preceding the normal serial data byte transmission.

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Interframe Message Time Setup for Proprietary UART-based Protocols

UART byte-based serial data signals with proprietary formats are often used. Given the flexibility of the setup of our trigger and decoder, it is often possible to use the Interframe Message Time Setup to “packetize” consecutive bytes into one Message Frame, and then trigger on in serial data in a particular byte location in the complete multi-byte Frame. Then, the UART decoder may be used to decode information as a complete Frame instead of individual Bytes.
## UART-RS232bus TD and UART-RS232bus TDME

### Definition

<table>
<thead>
<tr>
<th>Source and Protocol Setup</th>
<th>For UART:</th>
</tr>
</thead>
<tbody>
<tr>
<td>Select Source for Data</td>
<td></td>
</tr>
<tr>
<td>Select BitRate</td>
<td></td>
</tr>
<tr>
<td>Select # Data Bits (5-9)</td>
<td></td>
</tr>
<tr>
<td>Select Parity (Odd, Even, None)</td>
<td></td>
</tr>
<tr>
<td>Select # Stop Bits (1, 1.5, 2)</td>
<td></td>
</tr>
<tr>
<td>Select Bit Order (MSB or LSB)</td>
<td></td>
</tr>
<tr>
<td>Select Polarity (IdleLow or IdleHigh)</td>
<td></td>
</tr>
<tr>
<td>For RS-232:</td>
<td></td>
</tr>
<tr>
<td>Select Source for Data</td>
<td></td>
</tr>
<tr>
<td>Select BitRate</td>
<td></td>
</tr>
<tr>
<td>Select # Data Bits (5-8)</td>
<td></td>
</tr>
<tr>
<td>Select Parity (Odd, Even, None)</td>
<td></td>
</tr>
<tr>
<td>Select # Stop Bits (1, 1.5, 2)</td>
<td></td>
</tr>
</tbody>
</table>

### Trigger Capability

- **Format**: Hexadecimal or Binary
- **Trigger Setup**: Trigger on DATA or Parity Error

### DATA Setup

- **Format**: Hexadecimal or Binary
- **# Data Bytes = 0 to 12**. Data can be defined by nibble.
- **Binary**: Any combination of 0, 1, or X for 1-96 bits.
- **Frame definition permits definition of UART byte packets into a single long message package through a user-defined “Interframe Time” value. In this mode, a 12-bit Data pattern can be defined anywhere in a 2048 UART byte message frame.**

### DATA Condition Setup

- **<, <=, =, >, >=, <>**, IN RANGE, OUT OF RANGE, or DON’T CARE

### Bit Rates

- User-defined to any nominal value from 300 b/s to 10 Mb/s

### Decode + Search Capability

- **Format**: Hexadecimal, Binary, ASCII
- **Decode Setup**: Threshold definition required. Default is to Percent amplitude.
- **Select BitRate, # Data Bits (5 to 16), Parity (NONE, ODD, EVEN), # Stop Bits (1 or 2), Bit Order (MSB or LSB), and Polarity (IDLE HIGH or IDLE LOW) (for RS-232, no Bit Order or Polarity setup).**
- **Frame definition permits definition of UART byte packets into a single long (decoded) message package through a user-defined “Interframe Time” value.**

### Decode Input

- Any analog Channel, Memory or Math trace, and any Digital trace.
- **# of Decodes**: Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).

### Visual Aid

- Color Coding for START Bit, STOP Bit, PARITY Bit, and DATA bytes.
- Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.

### Table Configure, Export Table

- Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.

### Pattern Search

- Search for Previous or Next ERROR or DATA Byte in hexadecimal format.

## UART-RS232bus TDME only

### Measure / Graph Capability

- **Serial Data Digital-to-Analog Conversion (DAC)**
  - **Message to Value** measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion.
  - **Serial DAC Waveform** plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.

- **Timing Measurements**
  - **Message to Analog, Analog to Message, Message to Message, ΔMessage Time** (identical message on same decoder), **Time@Message** (time from trigger)
  - Serial Message may be defined by “ID = ” (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data.
  - Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.

- **Bus Status Measurements**
  - **Number of Messages, Message Bit Rate, Message Bus Load %**
  - Serial Message may be defined by “ID = ” (where applicable) and user-defined “DATA <=, <, =, >, >=, <>, in range, out of range” in any location in up to 2048 bits of data.

### Eye Diagram Capability

- **Setup**
  - Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.

- **Eye Parameters**
  - Eye Height, Eye Width, (Number of) Mask Hits

- **Eye Mask**
  - Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.

- **Failure Indication and Location**
  - Mask Failure Indication ON or OFF (ON = indicated with a red circle), Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.
**Key Features**

Symbolic trigger setup, decode, and data extraction and graph setup using (customer-supplied) DBC file

Error-frame red color decode highlight

DATA trigger pattern setup can be less than full bytes/nibbles and can be spread across bytes

Conditional ID definition (<, <=, =, >, >=, <=, IN RANGE, OUT of RANGE)

Supports 29-bit GM CAN Priority ID, Source ID, Parameter ID trigger and decode

**Symbolic (DBC) File Support**

CAN and CAN FD Symbolic decode options both support use of a customer-supplied DBC file for signal selection for triggering and CAN to Value serial data DAC setup. Additionally, the decode annotation is in Symbolic format as well, with complete message and signal structures described.

**Trigger Flexibly Across Data Bytes**

CAN remains the most used vehicle serial data bus. Many vehicle bus software architectures are very message dense, and data for a single message is spread across multiple data bytes. The hexadecimal and measurement toolsets permit isolation of specific bit-level data patterns in one or more data bytes, e.g., data location in bits 17-28 in data bytes 3, 4, and 5. This provides significant advantages in isolating the exact information or behavior you need. Symbolic message/signal setup is even simpler.
**SPECIFICATIONS**

<table>
<thead>
<tr>
<th>Definition</th>
<th>CANbus TD, CAN FDbus TD, CANbus TDM, CAN FDbus TDME</th>
<th>CANbus TDM Symbolic, CAN FDbus TDM Symbolic</th>
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</thead>
<tbody>
<tr>
<td><strong>Protocol Setup</strong></td>
<td>CANbus and CAN FDbus: Select Source. Select BitRate and Data BitRate. CAN FDbus Only: Select Frame Type (EDL) Both (X), CAN Standard (0), or CAN FD (1). For CAN FD, select ISO FRAME, and BR Select (BRS) Both(X), Normal(0), or FD(1).</td>
<td><strong>CANbus TDM Symbolic, CAN FDbus TDM Symbolic</strong></td>
</tr>
<tr>
<td><strong>Trigger Capability</strong></td>
<td>Hexadecimal or Binary. ID and DATA can be set up with different formats.</td>
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</tr>
<tr>
<td><strong>ID Setup</strong></td>
<td>Hexadecimal or Binary: Specify STD (11-bit) or EXT (29-bit) ID(s) with condition of &lt;=, &lt;, =, &gt;, &gt;=, &lt;&gt;, IN RANGE, OUT OF RANGE, or DONT CARE. Supports triggering when both 11-bit and 29-bit IDs are present on the bus.</td>
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<td><strong>DATA Setup</strong></td>
<td>Binary: Any combinations of 0, 1, or X for 1-64 (CAN) or 0-96 (CAN FD) bits. Data pattern can be any length and can be set to start at any location in the up to 8 (CAN) or 12 (CAN FD) Byte / 64 (CAN) or 96 (CAN FD) bit sequence. Byte Order Intel or Motorola format, Signed or Unsigned Data.</td>
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</tr>
<tr>
<td><strong>DATA Cond. Setup</strong></td>
<td>Select any combination of All Error Frames, Stuff Bit Errors, CRC Mismatch Errors. In CAN FDbus, also select Stuffbit Counter Error and Stuffbit Counter Parity Error.</td>
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<tr>
<td><strong>Error Frame Setup</strong></td>
<td>Supported for ID. Capability identical to ID Condition Setup.</td>
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</tr>
<tr>
<td><strong>Remote Frame Setup</strong></td>
<td>Nominal Bit Rate: 10, 25, 33.333, 50, 83.333, 100, 125, 250, 500 kb/s, or 1 Mb/s pre-defined nominal values, or user-defined from 10 kb/s to 1 Mb/s.</td>
<td>Nominal Bit Rate: 10, 25, 33.333, 50, 83.333, 100, 125, 250, 500 kb/s, or 1 Mb/s pre-defined nominal values, or user-defined from 10 kb/s to 1 Mb/s.</td>
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<tr>
<td><strong>Trigger Setup</strong></td>
<td>Any analog Channel or Digital input, or the EXT input.</td>
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<td><strong>Decode + Search Capability</strong></td>
<td>Hexadecimal or Binary.</td>
<td>Hexadecimal or Binary.</td>
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<tr>
<td><strong>Decode Setup</strong></td>
<td>Symbolic (Message and Signal level) or Hexadecimal. Symbolic decode requires user-provided DBC database file.</td>
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<td><strong># of Decode Wfms</strong></td>
<td>Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).</td>
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<td><strong>Visual Aid</strong></td>
<td>Color Coding for FRAME, ID, IDE, EDL, ESI, RTR, DLC, DATA, CRC, ACK, STUFF BITS, BIT INDEX, and ERRORS. Error Frames are decoded whenever possible, with uncorrupted portions decoded to Identify Type. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.</td>
<td>Color Coding for FRAME, ID, IDE, EDL, ESI, RTR, DLC, DATA, CRC, ACK, STUFF BITS, BIT INDEX and ERRORS. Symbolic includes textual Message name and physical Signal value with units. Error Frames are decoded whenever possible, with uncorrupted portions decoded to Identify Type. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.</td>
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<td><strong>Table Configure, Export Table</strong></td>
<td>Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.</td>
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<td><strong>Pattern Search</strong></td>
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</table>

**CANbus TDM, CANbus TDM Symbolic, CAN FDbus TDM, CAN FDbus TDM Symbolic only**

- **Measure / Graph Capability**
  - **Serial Data Digital-to-Analog Conversion (DAC)**
    - Message to Value: measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats; message filtering to specific IDs, and complete re-scaling with unit conversion. **Serial DAC Waveform** plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.
  - **Timing Measurements**
    - Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder). **Time@Message** (time from trigger). Serial Message may be defined by “ID =” (where applicable) and user-defined condition with condition <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.
  - **Bus Status Measurements**
    - Number of Messages, Message Bit Rate, Message Bus Load %
  - **Eye Diagram Capability**
    - Setup: Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.
    - Eye Parameters: Eye Height, Eye Width, (Number of) Mask Hits
    - Eye Mask: Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.
    - Failure Indication and Location: Mask Failure Indication ON or OFF (ON = indicated with a red circle).

Key Features

- LIN 1.3, 2.x and J2602 support
- Break (Start of Message), ID, ID+DATA, and Error Frame triggers
- Error-frame red color decode highlight
- Error-frame trigger can include some or all of Checksum, Header Parity, or Sync Byte types.
- Conditional ID definition (<, <=, =, >, >=, <>, IN RANGE, OUT of RANGE)
- Supports decode of buses with mixed LIN version traffic

Flexible Error Frame Trigger

Select to trigger on any combination of Checksum, Header Parity, or Sync Byte error frame types. Additional, Checksum Error allows further definition for Frame ID, LIN Version, and Number of Data Bytes.

Trigger Flexibly Across Data Bytes

Many vehicle bus software architectures are very message dense, and data for a single message is spread across multiple data bytes. Our LIN trigger and measurement toolsets permit isolation of specific bit-level data patterns in one or more data bytes, e.g., data location in bits 18-26 in data bytes 2 and 3. This provides significant advantages in isolating the exact information or behavior you need.
# SPECIFICATIONS

## LINbus TD and LINbus TDME

### Definition
- **Source and Protocol Setup**: Select Source. Select BitRate.

### Trigger Capability
- **Format**: Hexadecimal or Binary
- **Trigger Setup**: Trigger on (Sync) Break (Start of Message), Frame ID, Frame ID+DATA, Error Frame (Any combination of Checksum, Header Parity, or Sync Byte error frames)

### ADDRESS Setup
- **DATA Setup**: Specify one ADDRESS with condition of <=, <, =, >, >=, <> IN RANGE, OUT OF RANGE, or DON'T CARE.

### DATA Condition Setup
- **Error Setup**: Select any combination of Checksum Error, Header Parity, or Sync Byte types.
- **Bit Rates**: 1.2, 2.4, 4.8, 9.6, 10.417, 19.2 kb/s pre-defined nominal values, or user-defined to any nominal value from 300 b/s - 20 kb/s.

### Decode + Search Capability
- **Format**: Hexadecimal, Binary
- **Decode Setup**: Threshold definition required. Default is to Percent amplitude.
- **Decode Input**: Any analog Channel, Memory or Math trace, and any Digital trace.

### Measure / Graph Capability
- **Serial Data Digital-to-Analog Conversion (DAC)**: 
  - **Message to Value**: measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion.
  - **Serial DAC Waveform**: plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.

### Timing Measurements
- **Message to Analog, Analog to Message, Message to Message, ΔMessage Time** (identical message on same decoder), **Time@Message** (time from trigger)
- **Serial Message** may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <> IN RANGE, OUT OF RANGE in any location in up to 2048 bits of data.
- **Analog Signal** may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.

### Bus Status Measurements
- **Number of Messages, Message Bit Rate, Message Bus Load %**
  - **Serial Message** may be defined by "ID =" (where applicable) and user-defined "DATA <=, <, >, >=, <> in range, out of range" in any location in up to 2048 bits of data.

### Eye Diagram Capability
- **Setup**: Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.
- **Eye Parameters**: Eye Height, Eye Width, (Number of) Mask Hits
- **Eye Mask**: Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.
- **Failure Indication and Location**: Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.
Key Features

The most comprehensive oscilloscope-based FlexRay solution

Supports triggering for:
- Frame ID (Static and Dynamic)
- Frame Cycle Count
- Frame Qualifiers
- Symbols
- Errors

Physical Layer Measurements
- Propagation Delay
- Asymmetric Delay
- Truncation
- Jitter
- SI Voting

Supports 2.5, 5 and 10 Mb/s signals

Extensive Triggering Capabilities

Triggering on the complex FlexRay protocol is made easy. Set up a simple TSS (Start) symbol trigger with a single button press or trigger on any part of a FlexRay frame including ID, Cycle Count, Cycle Repetition Factor, and Frame Qualifier. FlexRay defined Symbols and Errors can also be incorporated into the trigger making it as simple or advanced as necessary. Conditional triggering can be set to trigger on any range of Frame IDs or Cycles.

Powerful Physical Layer Test

FlexRay eye diagram mask test overlays all the bits on FlexRay signal in an eye diagram with user-selected masks. Trigger on a specific Frame ID or range of IDs, or filter one long acquisition specific IDs, and show only those messages in the eye diagram. Supports SI Voting. Key timing parameters like Propagation Delay, Asymmetric Delay, Truncation and Jitter help you understand how signals propagate along the channel. Use statistics and histicons for deeper insight.
### FLEXRAYbus TD and FLEXRAYbus TDMP

#### Definition

<table>
<thead>
<tr>
<th>Source and Protocol Setup</th>
</tr>
</thead>
<tbody>
<tr>
<td>Select Source. Select BitRate. Select FlexRay Channel A or Channel B.</td>
</tr>
</tbody>
</table>

#### Trigger Capability

**Format**

- Hexadecimal or Binary for Frame ID.
- Decimal for Cycle Count.

**Trigger Setup**

- Trigger on TSS (Start), Frame ID, Cycle Count, Symbols, and Errors

**FRAME Setup**

- Specify Frame ID(s) in Hexadecimal or Binary with condition of \(<, =, >, \geq, \leq\), IN RANGE, OUT OF RANGE, or DON’T CARE.
- Specify Cycle Count from 0 to 63 with condition of \(<, =, >, \geq, \leq\), IN RANGE, OUT OF RANGE, or DON’T CARE. Specify Repetition Factor as 1, 2, 4, 8, 16, 32, or 64. Specify various Frame Qualifiers (Payload Preamble, Null Frame, Sync Frame, and Startup Frame) as 0, 1, or X (don’t care).

**DATA Setup**

- Hexadecimal:
  - # Data Bytes = 0 to 8. Data can be defined by nibble.
  - Binary: Any combination of 0,1, or X for 1-64 bits
  - Data pattern can be any length and can be set to start at any location in the up to 8 Byte / 64 bit sequence.

**Error Setup**

- Trigger on any combination of the following errors:
  - Frame Start Sequence (FSS) Error – triggers when the logic high time between the TSS and the first byte is too long. Byte Start Sequence (BSS) Error – triggers anytime the BSS pattern is not seen between bytes where expected.
  - Frame End Sequence (FES) Error – triggers when the FS is not seen after the last byte.
  - Header CRC Error, Payload CRC Error (select Payload Channel A or B).

**Symbol Trigger**

- Trigger on any combination of the following:
  - Channel Idle Delimiter (CID) Symbol, Collision Avoidance Symbol (CAS) and/or Media Access Test Symbol (MTS), or Wakeup Pattern (WUP)

**Bit Rates**

- 2.5, 5, or 10 Mb/s pre-defined nominal values, or user-defined nominal values in 1 Mb/s increments

**Trigger Input**

- Any analog Channel or the EXT input.

**Decode + Search Capability**

**Format**

- Hexadecimal, excepting Cycle Count (Decimal)

**Decode Setup**

- Threshold definition required for High and Low levels. Default is to Absolute (in volts) amplitude. Select Channel (A or B).

**Decode Input**

- Any analog Channel, Memory or Math trace, and any Digital trace.

**# of Decodes**

- Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).

**Visual Aid**

- Color Coding for FRAME, TSS, CID, FSS, Frame Qualifiers, Slot ID, Payload Length, Header CRC, Cycle Count, Data, BSS, Payload CRC and FES.
- Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.

**Table Configure, Export Table**

- Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.

**Pattern Search**

- Search by Previous or Next Frame, Next ID (hexadecimal format), or Next Error Frame.

---

### FLEXRAYbus TDMP only

#### Measure / Graph Capability

**Serial Data Digital-to-Analog Conversion (DAC)**

- **Message to Value** measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion. **Serial DAC Waveform** plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.

**Timing Measurements**

- **Message to Analog, Analog to Message, Message to Message, ΔMessage Time** (identical message on same decoder). **Time@Message** (time from trigger). **Serial Message** may be defined by "ID =" (where applicable) and user-defined DATA with condition \(<, =, >, \geq, \leq\), IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. **Analog Signal** may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.

**Bus Status Measurements**

- **Number of Messages, Message Bit Rate, Message Bus Load %**. Serial Message may be defined by "ID =" (where applicable) and user-defined "DATA \(<, =, >, \geq, \leq\), in range, out of range" in any location in up to 2048 bits of data.

#### Eye Diagram Capability

**Setup**

- Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.

**Eye Parameters**

- FlexRay PHY. Supports SI Voting (ON or OFF). With SI Voting ON, with voting selection for Positive Bit Length, Negative Bit Length (or both), and Filtered Input is possible. With SI Voting OFF, Any combination of Propagation Delay, Asymmetric Delay, Frame TSS Length Change, or Jitter.
- Eye Diagram: Eye Height, Eye Width, (Number of) Mask Hits

**Eye Mask**

- Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use. Standard FlexRay TP1, TP11 Bus Driver, TP11, and TP11 Active Star are also provided.

**Failure Indication and Location**

- Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.
Key Features

Symbolic decode with user-provided ULDF database file

Decode Viewing Control Selection

- 8+24
- 8+2+19+2+1
- User-defined

Decode Annotation includes:

- Frame
- ID
- Label
- Raw Bits
- SDI
- Data
- SSM
- Parity
- Symbolic Message and Symbols

Symbolic Decode Transparent Overlay

A unique and powerful way to view decoded data. Using a user-provided ULDF file, the label and equipment ID fields can be displayed in an intuitive and easy to interpret way. The ULDF Label file is a Comma Separated Variable (CSV) file that contains the ARINC429 token definitions. Any text editor can be used to create or modify the Label file, and there is no limitation as to how many signals can be defined for a given Label. Here, the specified converted data is Selected Mach = 645 Mach.

Symbolic Decode Protocol Table

Symbol data is then displayed in the protocol table. Quickly view valuable information for each ARINC 429 word, such as Label, SDI, Data, SSM, Parity, and Symbolic Message.
**ARINC429Bus DSYMBOLIC and ARINC429Bus DMESYMBOLIC**

<table>
<thead>
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<th><strong>Definition</strong></th>
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<td><strong>Decode Setup</strong></td>
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<td><strong>Decode Input</strong></td>
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<td># of Decodes</td>
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<td><strong>Serial DAC Waveform</strong></td>
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<td><strong>Timing Measurements</strong></td>
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<td><strong>Failure Indication and Location</strong></td>
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Key Features

Supports USB 2.0 Low, Full, or High speeds (1.x and 2.0)

Trigger on USB packet types:
- Token
- Data
- Handshake
- User-defined

Transaction triggering support

Comprehensive Protocol Error and Bus Event triggering

Comprehensive Search by Events, Packets, Transactions or Errors

The Most Comprehensive USB Trigger

Full support is provided for triggering on any type of Packet, even User-Defined Packets, with complete flexibility for address, endpoint, split type, hub, port, etc. Trigger on specific Data payloads in specific locations. OR any three Packets in a single trigger condition. Create a USB Transaction trigger with any allowed combination of Token, Data, Handshake, and User-Defined packets. Advanced capability like this is usually only found in a dedicated protocol analyzer!

Search and Zoom

The powerful search engine of the USB 1.x/2.0 decode package can quickly find an Event, Packet, Transaction, or Protocol Error. Search through a long record of decoded data by entering any of the 45 available search criteria by entering a value or simply finding the next occurrence. For example, search through a long record to find a glitch that is frequently occurring after each EOP.
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Key Features

Most comprehensive MIL-STD-1553 oscilloscope trigger available
- Transfers
- Command Words
- Data Words
- Status Words
- Error Words
- Response Times
- Intermessage Gap Times

Conditional ADDRESS definition
(<, <=, =, >, >=, <> IN RANGE, OUT of RANGE)

Completely isolate a specific RT Address, Sub Address, Data Value, and Mode Code

Support for MIL-STD-1553 versions A and B

Highly Flexible and Powerful Triggering

The MIL-STD-1553 trigger can be configured at the transfer or word level to provide the right level of triggering. In addition, error triggers are able to locate the cause of protocol errors at either the transfer or word level. Word level triggering allows conditional RT Address and Sub Address entry.

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MIL-STD-1553 TD and MIL-STD-1553 TDME

<table>
<thead>
<tr>
<th>Definition</th>
<th>Source Setup</th>
<th>Select Source</th>
</tr>
</thead>
<tbody>
<tr>
<td>Trigger Capability</td>
<td>Format</td>
<td>Hexadecimal or Binary (Decimal for Word Count).</td>
</tr>
<tr>
<td>Trigger Setup</td>
<td>Trigger on ANY TRANSFER, a COMMAND WORD, STATUS WORD, DATA WORD, or ALL WORDS, an ERROR, a RESPONSE TIME, or an INTERMESSAGE GAP TIME. TRANSFERS may be further qualified by selecting the message type BC-RT, RT-BC, RT-RT, MODE COMMAND, MODE COMMAND &amp; DATA (XMIT), MODE COMMAND AND DATA (RCV), various BROADCASTS (BC-RT(S), RT-RT(S), MODE COMMAND, and MODE COMMAND AND DATA).</td>
<td></td>
</tr>
<tr>
<td>Address Setup</td>
<td>For COMMAND WORD trigger specify 5-bit Remote Terminal (RT) Address ID(s) or Sub Address(es) with condition of &lt;=, &lt;, =, &gt;, &gt;=, &lt;&gt; IN RANGE, OUT OF RANGE, or DON'T CARE, specify Transmit/Receive bit setting of 0, 1, or X (don't care). For STATUS WORD trigger, specify 6-bit RT Address(es) ID(s) with condition of &lt;=, &lt;, =, &gt;, &gt;=, &lt;&gt; IN RANGE, OUT OF RANGE, or DON'T CARE; Specify Status Word bits as 0, 1, or X (don't care) for Message Error, Instrumentation, Service Request, Broadcast Command Received, Busy, Subsystem Flag, Dynamic Bus Control Acceptance, or Terminal Flag. For any TRANSFER containing an RT Address or Sub Address, setup is identical to that specified above. Settable in Hexadecimal or Binary format in all cases.</td>
<td></td>
</tr>
</tbody>
</table>
MIL-STD-1553 TD and MIL-STD-1553 TDME (Cont’d)

**DATA Setup**
Data Word Count: In any TRANSFER, specify Data Word Count in decimal format up to 32 data words.
DATA WORD or TRANSFER Data Setup (Hexadecimal): # Bytes = up to 2 (one Data Word) byte length, settable by nibble.
DATA WORD or TRANSFER Data Setup (Binary): Any combination of 0,1, or X for 1-16 bits.
Data pattern can be set to start at any location in an up to 2 Byte / 16 bit sequence (in a DATA WORD) or an up to 64 Byte / 512 bit sequence (in a TRANSFER)

**DATA Condition Setup**
Data Word Count: <=, <, =, >, >=, or <>
Data Setup: <=, <, =, >, =, IN RANGE, OUT OF RANGE, or DON’T CARE

**Mode Command Setup**
TRANSFER MODE COMMANDS and COMMAND WORDS may be qualified by selecting a Mode Code (0 to 31, with description) with a Mode Code condition of <=, <, =, >, =, or <>.

**Status Setup**
In any TRANSFER or STATUS WORD, select 0, 1, or X (don’t care) for various Status Word bits. Select for: Message Error, Instrumentation, Service Request, Broadcast Command Received, Busy, Subsystem Flag, Dynamic Bus Acceptance, and Terminal Flag

**Error Setup**
Select one or more Word Level or Transfer Level errors using a check box. Word Level error selection: Invalid Sync, Manchester Error, Idle Error, Parity Error. Transfer Level error selection: Bad Word Count, Address Mismatch, Non-contiguous Data, Sync Error.

**Other Setups**
Response Time Setup: Conditional Setup <=, <, in range, out of range, Value Setup: 0 to 32,752 microseconds.
Intermessage Gap Setup: Conditional Setup <=, <, in range, out of range, Value Setup: 0 to 32,752 microseconds.

**Bit Rates**
1 Mb/s, pre-defined nominal value.

**Trigger Input**
Any analog Channel or the EXT input.

**Decode + Search Capability**

**Format**
Hexadecimal, Binary, Decimal (Binary not available for Address).

**Decode Setup**
Threshold definition required for High and Low levels. Default is to Absolute (in volts) amplitude. Select Table (Display) Mode (WORD or TRANSFER), Define Response Time and InterMessage Gap Time limits.

**Decode Input**
Any analog Channel, Memory or Math trace, and any Digital trace.

**# of Decodes**
Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).

**Visual Aid**
Color Coding for Message, Word, Sync bits, RTA Address and SubAddress bits, Receive/Transmit bit, Data Count bits, Data (Payload) bytes and Single-bit Condition Codes, Reserved bits, Response Time Check and Inter-Message Gap Time, and Word and Transfer Level Error Codes.
Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.

**Table Configure, Export Table**
Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.

**Pattern Search**
Search for Previous or Next Index, Time, Message, Transaction, Type, Summary, Sync, RT Address, T/R, SubAddress, Count, ModeCode, Parity, Response Time, RT Address ACK, Message Error, Inst, SRQ, Reserved, Broadcast Rx, Busy, SubSystem Flag, Dynamic Bus Access, Terminal Flag, Data, IMG, or Status

**MIL-STD-1553 TDME only**

**Measure / Graph Capability**

**Serial Data Digital-to-Analog Conversion (DAC)**
Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion. Serial DAC Waveform plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.

**Timing Measurements**
Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Time@Message (time from trigger), Serial Message may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, =, =>, IN RANGE, OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.

**Bus Status Measurements**
Number of Messages, Message Bit Rate, Message Bus Load %. Serial Message may be defined by "ID =" (where applicable) and user-defined "DATA <=, <, =, >, =, =>, in range, out of range" in any location in up to 2048 bits of data.

**Eye Diagram Capability**

**Setup**
Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.

**Eye Parameters**
Eye Height, Eye Width, (Number of) Mask Hits

**Eye Mask**
Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.

**Failure Indication and Location**
Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.
Key Features

Supports triggers in SDR, HDR-DDR, and Legacy I2C operating modes

Provides triggering in “ANY” supported operating mode

Provides for complex triggering with defined Address, R/W (direction), Common Command Code, and Data.

Conditional data definition (<, <=, =, >, >=, !=, INRANGE, OUT of RANGE).

Use analog or digital (MSO) inputs for acquisition and triggering.

Comprehensive and Powerful I3C Trigger

Capability is provided to permit triggering on any MIPI-defined allowable command sequence, and also on user-defined command sequences, with full capability to define specific data values or ranges of data values.

Extract Digital Sensor Data and Plot it as a Waveform

Embedded digital sensor data can be difficult to understand by reviewing hexadecimal values in a table. The I3Cbus TDME option provides ability to extract digital data from a defined data location in specific I3C messages and convert them to analog values that are then plotted to resemble an analog waveform.
I3Cbus TD and I3Cbus TDME

**Definition**

**Source Setup**
Select Source for Clock and Data.

**Trigger Capability**

**Format**
Hexadecimal or Binary. ADDRESS and DATA can be set up with different formats.

**Trigger Setup**
Select from operating modes to trigger on the following:
- SDR: Start (S), Stop (P), Restart (SR), Direct Frame, Broadcast Frame, SDR Frame Length, Direct Address + CCC + Data, Broadcast Address + CCC + Data, SDR Errors
- HDR-DDR: Enter, Exit, Restart, Frame Length, Address + Cmd + Data, Preamble, Errors
- I2C Legacy: START, ReSTART, STOP, Missing ACK, ADDR, ADDR+DATA, ADDR+DATA FRAME LENGTH, EEPROM DATA TRANSFER

**ADDRESS Setup**
Specify one ADDRESS with condition of "="
7 or 10 bit ADDRESS supported with full Read, Write, or R/W="Don’t Care" selectability on both 7 and 10 bit ADDRESSes. Choose to Trigger on address values that include/don’t include R/W bit in address value.

**DATA Setup**
Direct Address + CCC + Data, Broadcast Address + CCC + Data, ADDRESS+DATA, Any Address + Data, and HDR-DDR Address + Cmd + Data Trigger Types:
- Hexadecimal: # Data Bytes = 0 to 12. Data can be defined by nibble.
- Binary: Any combination of 0,1, or X for 1-96 bits

**DATA Conditions**
<=, <, =, >, >=, <> IN RANGE, OUT OF RANGE, or DON’T CARE

**ACK Conditions**
For any Direct Address + CCC + Data, Broadcast Address + CCC + Data, ADDR+CC+Data, Addr + Data, or Any Addr + Data setup, select an ACK Condition of ACK, NO ACK, and DON’T CARE.

**Bit Rates**
Full range of speeds for I3C specification for SDR, HDR-DDR, and I2C legacy operation modes.

**Trigger Input**
Any analog Channel or Digital input, or the EXT input. Clock may be input to EXT to conserve available analog Channels.

**Decode + Search Capability**

**Format**
Hexadecimal, Binary, ASCII

**Decode Setup**
Threshold definition required. Default is to Percent amplitude.
Choose to Decode address values including/not including the R/W bit in address value.

**Decode Input**
Any analog Channel, Memory or Math trace, and any Digital trace.
Clock channel may be turned OFF and data will still decode (reduces screen clutter).

**# of Decodes**
Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).

**Visual Aid**
Color Coding for FRAME, START/ReSTART bit, ADDR, R/W, DATA, ACK, NACK, and STOP bit.
Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.

**Table Configure, Export Table**
Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.

**Pattern Search**
Search for Previous or Next ADDRESS, PACKET, or DATA in hexadecimal format.

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I3Cbus TDME only

**Measure / Graph Capability**

**Serial Data**
Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion.

**Serial DAC Waveform**
Plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.

**Timing Measurements**
Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Time@Message (time from trigger)
Serial Message may be defined by “ID = “ (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RANGE, OF RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.

**Bus Status Measurements**
Number of Messages, Message Bit Rate, Message Bus Load %
Serial Message may be defined by “ID = “ (where applicable) and user-defined “DATA <=, <, =, >, >=, <>, in range, out of range” in any location in up to 2048 bits of data.

**Eye Diagram Capability**
Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.

**Eye Parameters**
Eye Height, Eye Width, (Number of) Mask Hits.

**Eye Mask**
Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.

**Failure Indication and Location**
Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.
Key Features

Trigger and Decode Commands, Command Sequences and Error Frames

User-defined Command Frame/Sequence – trigger on non-standard frames.

Conditional data definition (<, <=, =, >, >=, <>), INRANGE, OUT of RANGE).

Use analog or digital (MSO) inputs for acquisition and triggering.

Full arbitration sequence support and for all sequences with pauses.

Comprehensive and Powerful SPMI Trigger

Capability is provided to permit triggering on any MIPI-defined allowable command sequence, and also on user-defined command sequences, with full capability to define specific data values or ranges of data values.

Eye Diagrams Identify Physical Layer Issues

Quickly create an eye diagram to intuitively show bit transitions and physical layer signal quality. In this 10 ms acquisition, it is easy to assess the signal quality and find the irregular bit transitions (faint purple lines).
## SPECIFICATIONS

### SPMibus TD and SPMibus TDME

<table>
<thead>
<tr>
<th>Specification</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Definition</strong></td>
<td>Select Source for Clock and Data.</td>
</tr>
<tr>
<td><strong>Source and Protocol Setup</strong></td>
<td>Hexadecimal or Binary</td>
</tr>
<tr>
<td><strong>Trigger Capability</strong></td>
<td>Trigger on SSC, Command (Reset, Sleep, Shutdown, Wakeup), Command Sequence (Master Read, Master Write, Block Master Read, Block Slave Read, Extended Register Read, Extended Register Write, Extended Register Read Long, Extended Register Write Long, Register Read, Register Write, Register 0 Write, Authenticate, Transfer Bus Ownership, User-defined), or Errors (OR any combination of Command Parity Error, Acknowledgement Error, Data Parity Error, Frames Error).</td>
</tr>
<tr>
<td><strong>Data Setup</strong></td>
<td>Command Sequence Trigger Type: Hexadecimal: # Data Bytes = 0 to 16. Data can be defined by nibble. Binary: Any combination of 0, 1, or X for 1-128 bits*</td>
</tr>
<tr>
<td><strong>Data Condition Setup</strong></td>
<td>&lt;=, &lt;, =, &gt;, &gt;=,  &lt;&gt;, IN RANGE, OUT OF RANGE, or DONT CARE</td>
</tr>
<tr>
<td><strong>Bit Rates</strong></td>
<td>Any, up to 26 Mb/s. Auto-detected from clock signal.</td>
</tr>
<tr>
<td><strong>Trigger Input</strong></td>
<td>Any analog Channel or Digital input, or the EXT input. Clock may be input to EXT to conserve available analog Channels.</td>
</tr>
<tr>
<td><strong>Decode + Search Capability</strong></td>
<td>Hexadecimal, with selection for Bit Viewing (Index or State).</td>
</tr>
<tr>
<td><strong>Table Configure, Export Table</strong></td>
<td>Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.</td>
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<td><strong>Pattern Search</strong></td>
<td>Basic search for Previous or Next INDEX, TIME, MESSAGE (value), SSC, SLAVE (id), COMMAND, BYTE COUNT CODE, REGISTER, DATA (value), NBITS, or STATUS. Advanced search allows complex criteria using boolean and/or logic to combine up to three different searches.</td>
</tr>
</tbody>
</table>

### SPMibus TDME only

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<td><strong>Bus Status Measurements</strong></td>
<td>Number of Messages, Message Bit Rate, Message Bus Load % Serial Message may be defined by &quot;ID = &quot; (where applicable) and user-defined &quot;DATA &lt;=, &lt;, =, &gt;, &gt;=,  &lt;&gt;, in range, out of range&quot; in any location in up to 2048 bits of data.</td>
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<td><strong>Failure Indication and Location</strong></td>
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</tr>
</tbody>
</table>
## Compatibility

<table>
<thead>
<tr>
<th>Embedded Computing</th>
<th>WaveSurfer 3000z</th>
<th>WaveSurfer 510, HDO4000A</th>
<th>WaveRunner 8000 WaveRunner 9000</th>
</tr>
</thead>
<tbody>
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<td>I²C</td>
<td>TD</td>
<td>TD, TDME</td>
<td></td>
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<tr>
<td>SPI</td>
<td>EMB bundle</td>
<td>TD</td>
<td></td>
</tr>
<tr>
<td>UART-RS232</td>
<td>TD</td>
<td>TD, TDME</td>
<td></td>
</tr>
<tr>
<td>USB 2.0 HSIC</td>
<td>D</td>
<td>D</td>
<td></td>
</tr>
<tr>
<td>EMB bundle (I²C, SPI, UART-RS232)</td>
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<td>TD, TDME</td>
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<tr>
<td>CAN</td>
<td>AUTO bundle</td>
<td>TD</td>
<td></td>
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<tr>
<td>CAN FD⁶</td>
<td>TD</td>
<td>TD, TDME, TDME Symbolic</td>
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<tr>
<td>FlexRay</td>
<td>TD</td>
<td>TD</td>
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<tr>
<td>LIN</td>
<td>AUTO bundle</td>
<td>TD</td>
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<tr>
<td>SENT</td>
<td>D</td>
<td>D</td>
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<tr>
<td>AUTO bundle</td>
<td>TD⁴</td>
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<tr>
<td>ARINC 429</td>
<td>-</td>
<td>D Symbolic</td>
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<tr>
<td>MIL-STD-1553</td>
<td>-</td>
<td>D Symbolic, DME Symbolic</td>
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<tr>
<td>SpaceWire</td>
<td>-</td>
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<td></td>
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<tr>
<td>Ethernet (10/100Base-T)</td>
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<td>D</td>
<td></td>
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<td>USB 2.0</td>
<td>-</td>
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<tr>
<td>DigRF 3G</td>
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<td>D, DMP⁵</td>
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<td>TD</td>
<td></td>
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<tr>
<td>Audio (I³S, LJ, RJ, TDM)</td>
<td>TD</td>
<td>TD</td>
<td></td>
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<tr>
<td>Manchester</td>
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<td></td>
</tr>
<tr>
<td>NRZ</td>
<td>-</td>
<td>D</td>
<td></td>
</tr>
</tbody>
</table>

### Notes

1. DME, DME Symbolic, DMP, and DG packages are available for these models through the Teledyne LeCroy Service Department - consult factory.
2. Trigger and Decode are available through separately orderable options - consult factory.
3. Measure/Graph ("M") capability is available for these protocols through purchase of the Decoder ("D" option) and ProtoBus MAG Serial Debug Toolkit (a separate orderable item).
4. AUTO bundle contains CAN and LIN trigger and decode ("TD") when purchased with WaveSurfer 3000z, and contains CAN, LIN and FlexRay trigger and decode ("TD") when purchased with WaveSurfer 10, and HDO4000A.
5. Available on some bandwidth models only.
6. CAN FDbus options support both CAN FD and the legacy CAN protocol.
<table>
<thead>
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<th>HDO6000A, HDO8000A, MDA800A</th>
<th>WavePro HD</th>
<th>WaveMaster 8 Zi-B</th>
<th>LabMaster 10 Zi-A</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>PC</strong></td>
<td>TD, TDME</td>
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<tr>
<td><strong>DigRF 3G</strong></td>
<td>D, DM³</td>
<td>D, DM³</td>
<td>D, DM³</td>
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<td><strong>DigRF v4</strong></td>
<td>D, DM³</td>
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<td><strong>SPMI</strong></td>
<td>TD, TDME</td>
<td>TD, TDME</td>
<td>D, DME</td>
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<td><strong>PC</strong></td>
<td>TD, TDME</td>
<td>TD, TDME</td>
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<tr>
<td><strong>Audio (I²S, LJ, RJ, TDM)</strong></td>
<td>TD, TDG</td>
<td>TD, TDG</td>
<td>D, DG¹</td>
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<td><strong>Manchester</strong></td>
<td>D</td>
<td>D</td>
<td>D</td>
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<tr>
<td><strong>NRZ</strong></td>
<td>D</td>
<td>D</td>
<td>TD²</td>
</tr>
</tbody>
</table>

1 – DME, DME Symbolic, DMP, and DG packages are available for these models through the Teledyne LeCroy Service Department - consult factory
2 – Trigger and Decode are available through separately orderable options - consult factory
3 – Measure/Graph ("M") capability is available for these protocols through purchase of the Decoder ("D" option) and ProtoBus MAG Serial Debug Toolkit (a separate orderable item)
4 – AUTO bundle contains CAN and LIN trigger and decode ("TD") when purchased with WaveSurfer 3000z, and contains CAN, LIN and FlexRay trigger and decode ("TD") when purchased with WaveSurfer 10, and HDO4000A.
5 – Available on some bandwidth models only.
6 – CAN FDbus options support both CAN FD and the legacy CAN protocol.
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Customer Service
Teledyne LeCroy oscilloscopes and probes are designed, built, and tested to ensure high reliability. In the unlikely event you experience difficulties, our digital oscilloscopes are fully warranted for three years and our probes are warranted for one year. This warranty includes:

• No charge for return shipping  • Long-term 7-year support  • Upgrade to latest software at no charge