

# **Mid-bus Probe** for PCI Express® 3.0

## **Key Features**

- Supports PCI Express 3.0
- Supports SRIS Clocking
- Captures all bus traffic at data rates up to 8 GT/s
- Covers lane widths up to x8 using one probe; up to x16 using two probes
- Supports full-size or half-size probe footprints
- Provide guick and easy probe attachment with minimal signal perturbation
- Can be used with Teledyne LeCroy's Dish Interface Board to provide convenient SMP probe connection in early prototyping
- Lane Swizzling feature in Summit analyzers allows flexibility in PCB layout





Mid-bus probe provides quick, easy and secure connection to PCB connector. Illustration shows optional dish interface board for prototype use.

Teledyne LeCroy's PCIe® 3.0 (Gen3) mid-bus probe supports quick and easy probing through a simple probe footprint designed into the PCB.

Mid-bus probes are used by system designers to probe embedded bus signals (e.g., for serial data buses that run between chips on a single circuit board) or simply as a convenient means to access bus signals with a probe connector. The probe is easily attached to an anchor connector mounted on top of a midbus footprint that is laid out on the target test system board.

The Teledyne LeCroy mid-bus probe uses a full-size connector that supports PCIe x8 lanes at data rates up to 8 GT/s. Two mid-bus probes can be connected

to a Summit<sup>™</sup> T3-16 Analyzer and will support x16 lane widths. These probes can be used in conjunction with the unique lane swizzling feature on the Summit protocol analyzers, which allows probe signals to be reorganized logically, to give developers additional flexibility in PCB layout.

System Compatibility	
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Summit T3-16	✓
Summit T3-8	✓
Summit T34	✓
Summit T28	
Summit T24	

## **Specifications**

iPass Cable to 1.0 meter (39") Analyzer

PCIe 3.0 Probe Pod 150 x 100 x 27 mm (6" x 4" x 1.1")

Ribbon Cable to **Probe** 

650 mm (25.5")

### **Ordering Information**

**Product Description** 

Summit T3-16 Analyzer (x16 lane width)

Summit T3-8 Analyzer (x8 lane width)

Summit T34 Analyzer (x4 lane width)

**Product Code** 

PE050AAA-X

PE060AAA-X

PE080AAA-X

#### **Product Description**

Gen3 x8 Full-Size Mid-bus Probe Kit

(includes Mid-bus Pod, iPass Y-Cable, ribbon probe cable, reference clock cable and daisy chain cable)

PCIe Gen3 x8 Dish for Mid-bus Probe Full-size Gen3 Probe Connector

PE038UIA-X PE047UIA-X

**Product Code** 

PE090ACA-X



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